

REVISIONS																					
LTR	DESCRIPTION										DATE (YR-MO-DA)				APPROVED						
A	Add device types 02-08. Table I changes.										94-10-24				M. A. Frye						
B	Add case outline X. - drw										99-06-02				Raymond Monnin						
C	Update drawing to current requirements. Editorial changes throughout. - drw										04-10-20				Raymond Monnin						
D	Redrawn. Update paragraphs to MIL-PRF-38535 requirements. - drw										16-11-17				Charles F. Saffle						
E	Drawing updated to reflect current MIL-PRF-38535 requirements. -rrp										22-08-15				James R. Eschmeyer						
																					
THE ORIGINAL FIRST SHEET OF THIS DRAWING HAS BEEN REPLACED.																					
Revision Status of Sheets																					
REV																					
SHEET																					
REV	E	E	E	E	E	E	E	E	E												
SHEET	1	2	3	4	5	6	7	8	9												
PMIC N/A																					
STANDARD MICROCIRCUIT DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE										PREPARED BY RICK C. OFFICER				DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 https://www.dla.mil/LandandMaritime							
										CHECKED BY CHARLES E. BESORE											
										APPROVED BY MICHAEL A. FRYE				MICROCIRCUIT, LINEAR, CMOS, DUAL, RS232, TRANSCIEVER, MONOLITHIC SILICON							
										DRAWING APPROVAL DATE 90-02-10											
AMSC N/A										REVISION LEVEL E				SIZE A	CAGE CODE 67268	5962-89877					
														SHEET 1 OF 9							

1. SCOPE

1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:

<u>5962-89877</u>	<u>01</u>	<u>E</u>	<u>A</u>
Drawing Number	Device type (see 1.2.1)	Case outline (see 1.2.2)	Lead finish (see 1.2.3)

1.2.1 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	MAX232	Dual CMOS transceivers
02	MAX230	5 CMOS transceivers with power shutdown
03	MAX231	5 V/12 V dual CMOS transceivers
04	MAX234	Quad CMOS transceivers
05	MAX236	CMOS RS-232 4 transmitter, 3 receiver with power shutdown and receiver three state enable
06	MAX237	CMOS RS-232 5 transmitter, 3 receiver
07	MAX238	CMOS RS-232 4 transmitter, 4 receiver
08	MAX239	5 V/12 V CMOS RS-232 3 transmitter, 5 receiver with three state receiver enable

1.2.2 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
C	GDIP1-T14 or CDIP-T14	14	Dual-in-line
E	GDIP1-T16 or CDIP-T16	16	Dual-in-line
R	GDIP1-T20 or CDIP-T20	20	Dual-in-line
J	GDIP1-T24 or CDIP-T24	24	Dual-in-line
2	CQCC1-N20	20	Square leadless chip carrier
X	CDFP4-F16	16	Flatpack

1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

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1.3 Absolute maximum ratings.

Supply voltage (V_{CC})	-0.3 V dc to +6 V dc
Positive power supply voltage (+V)	($V_{CC} - 0.3$ V dc) to +14 V dc
Negative power supply voltage (-V)	+0.3 V dc to -14 V dc
Driver input voltage	-0.3 V dc to ($V_{CC} + 0.3$ V dc)
Receiver input voltage	-30 V dc to +30 V dc
Driver output voltage	(V+ + 0.3 V dc) to (V- - 0.3 V dc)
Receiver output voltage	-0.3 V dc to ($V_{CC} + 0.3$ V dc)
Short circuit duration, driver outputs	Continuous
Power dissipation at $T_A = +70^\circ\text{C}$ (P_D): 1/ 2/	
Case outlines C and 2	727 mW
Case outline E	800 mW
Case outline J	1000 mW
Case outline R	889 mW
Thermal resistance, junction-to-case (θ_{JC})	See MIL-STD-1835
Thermal resistance, junction-to-ambient (θ_{JA}):	
Case outlines C and 2	110°C/W
Case outline E	100°C/W
Case outline J	80°C/W
Case outline R	90°C/W
Maximum junction temperature (T_J)	+150°C
Storage temperature range	-65°C to +160°C
Lead temperature (soldering, 10 seconds)	+300°C

1.4 Recommended operating conditions.

Supply voltage range (V_{CC})	+4.5 V dc to +5.5 V dc
Ambient operating temperature range (T_A)	-55°C to +125°C
Maximum receiver output enable time 3/	400 ns
Maximum receiver output disable time 3/	250 ns

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://quicksearch.dla.mil/>).

1/ Must withstand the added P_D due to short circuit test; e.g., I_{OS} .

2/ Above $T_A = +70^\circ\text{C}$, cases C and 2 derate at 9.09 mW/°C, case E derates at 10 mW/°C, case R derates at 11.11 mW/°C, and case J derates at 12.5 mW/°C.

3/ These typical values for device types 05 and 08 only.

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2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 Case outline. The case outline shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 Certification/compliance mark. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DLA Land and Maritime-VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DLA Land and Maritime-VA shall be required for any change that affects this drawing.

3.9 Verification and review. DLA Land and Maritime, DLA Land and Maritime's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions ^{1/} -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Output voltage swing	V _{OUT}	All transmitter outputs loaded with 3 kΩ to GND	1, 2, 3	All	-5.0	+5.0	V
V _{CC} power supply current	I _{CC}	No load	1	01		10	mA
				02, 04, 05, 06, 07		15	
				03, 08		1	
V+ power supply current	I+	No load	1	03		5	mA
				08		15	
Shutdown supply current	I _{SHDN}		1	02, 05		10	μA
RS-232 TRANSMITTERS							
Low level input voltage	V _{IL}	T _{IN}	1, 2, 3	All		0.8	V
		$\overline{\text{EN}}$		05, 08		0.8	
		SHDN		02, 05		0.8	
High level input voltage	V _{IH}	T _{IN}	1, 2, 3	All	2.0		V
		$\overline{\text{EN}}$		05, 08	2.4		
		SHDN		02, 05	2.4		
Logic pullup current low	I _{IL}	T _{IN} = 0 V	1, 2, 3	All		200	μA
Output short circuit current	I _{OST}	V _{OUT} = 0 V Sourcing current	1	01		±25	mA
		V _{OUT} = 0 V Sinking current		02, 03, 04, 05, 06, 07, 08		±45	
Transmitter output resistance	R _{OUT}	V _{CC} = V+ = V- = 0 V, V _{OUT} = ±2 V	1, 2, 3	All	300		Ω

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – continued.

Test	Symbol	Conditions <u>1/</u> -55°C ≤ TA ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit	
					Min	Max		
RS-232 RECEIVERS								
Receiver input voltage operating range	VR		1, 2, 3	01, 03, 05, 06, 07, 08	-30	+30	V	
RS-232 input threshold low	VTL	Normal operation, VCC = 5 V	1	01, 03, 05, 06, 07, 08	0.8		V	
			2, 3		0.4			
RS-232 input threshold high	VTH		1			2.4		
			2, 3			3.0		
Receiver input hysteresis	RH	VCC = 5 V	1, 2, 3	01, 03, 05, 06, 07, 08	0.2	1.0	V	
Receiver input resistance	RI	VCC = 5 V, TA = +25°C	1	01, 03, 05, 06, 07, 08	3.0	7.0	kΩ	
Receiver output high voltage	VOH	IOUT = -1.0 mA	1, 2, 3	01, 03, 05, 06, 07, 08	3.5		V	
Receiver output low voltage	VOL	IOUT = 3.2 mA	1, 2, 3	01, 03		0.4	V	
		IOUT = 1.6 mA		05, 06, 07, 08		0.4		
TTL/CMOS output leakage current	IL	0 V ≤ ROUT ≤ VCC, $\overline{\text{EN}}$ = VCC	1, 2, 3	05, 08		±10	μA	
AC TESTS								
Propagation delay time	tp	RS-232 IN to TTL/CMOS OUT, CL = 150 pF	9, 10, 11	01, 03, 05, 06, 07, 08		10	μs	
Transition region slew rate	tsr	CL = 50 pF to 2500 pF, RL = 3 kΩ to 7 kΩ, VCC = +5 V	9	02, 04, 05, 06, 07, 08	3.0	30	V/μs	
		Measure from +3.0 V to -3.0 V or -3.0 V to +3.0 V		01, 03	1.5	30		

^{1/} V_{CC} = +5 V ±10% for device types 01, 02, 04, 05, 06, and 07. V_{CC} = +5 V ±10%, V₊ = 9.0 V to 13.2 V for device types 03 and 08.

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Device types	01		02	03	04	05	06	07	08
Case outlines	E, X	2	R	C	E	J	J	J	J
Terminal number	Terminal symbol								
1	+C1	NC	T3 _{OUT}	+C	T1 _{OUT}	T3 _{OUT}	T3 _{OUT}	T2 _{OUT}	R1 _{OUT}
2	+V	+C1	T1 _{OUT}	-C	T2 _{OUT}	T1 _{OUT}	T1 _{OUT}	T1 _{OUT}	R1 _{IN}
3	-C1	+V	T2 _{OUT}	-V	T2 _{IN}	T2 _{OUT}	T2 _{OUT}	R2 _{IN}	GND
4	+C2	-C1	T2 _{IN}	T2 _{OUT}	T1 _{IN}	R1 _{IN}	R1 _{IN}	R2 _{OUT}	V _{CC}
5	-C2	+C2	T1 _{IN}	R2 _{IN}	GND	R1 _{OUT}	R1 _{OUT}	T1 _{IN}	+V
6	-V	NC	GND	R2 _{OUT}	V _{CC}	T2 _{IN}	T2 _{IN}	R1 _{OUT}	+C
7	T2 _{OUT}	-C2	V _{CC}	T2 _{IN}	+C1	T1 _{IN}	T1 _{IN}	R1 _{IN}	-C
8	R2 _{IN}	-V	+C1	T1 _{IN}	+V	GND	GND	GND	-V
9	R2 _{OUT}	T2 _{OUT}	+V	R1 _{OUT}	-C1	V _{CC}	V _{CC}	V _{CC}	R5 _{IN}
10	T2 _{IN}	R2 _{IN}	-C1	R1 _{IN}	+C2	+C1	+C1	+C1	R5 _{OUT}
11	T1 _{IN}	NC	+C2	T1 _{OUT}	-C2	+V	+V	+V	R4 _{OUT}
12	R1 _{OUT}	R2 _{OUT}	-C2	GND	-V	-C1	-C1	-C1	R4 _{IN}
13	R1 _{IN}	T2 _{IN}	-V	V _{CC}	T3 _{IN}	+C2	+C2	+C2	T3 _{OUT}
14	T1 _{OUT}	T1 _{IN}	T3 _{IN}	+V	T4 _{IN}	-C2	-C2	-C2	EN
15	GND	R1 _{OUT}	T4 _{IN}	---	T4 _{OUT}	-V	-V	-V	NC
16	V _{CC}	NC	T5 _{OUT}	---	T3 _{OUT}	R3 _{IN}	R3 _{IN}	R4 _{IN}	T3 _{IN}
17	---	R1 _{IN}	SHDN	---	---	R3 _{OUT}	R3 _{OUT}	R4 _{OUT}	R3 _{OUT}
18	---	T1 _{OUT}	NC	---	---	T3 _{IN}	T3 _{IN}	T2 _{IN}	R3 _{IN}
19	---	GND	T5 _{IN}	---	---	T4 _{IN}	T4 _{IN}	T3 _{IN}	T1 _{OUT}
20	---	V _{CC}	T4 _{OUT}	---	---	EN	T5 _{OUT}	T4 _{OUT}	T2 _{OUT}
21	---	---	---	---	---	SHDN	T5 _{IN}	T4 _{IN}	R2 _{IN}
22	---	---	---	---	---	R2 _{OUT}	R2 _{OUT}	R3 _{OUT}	R2 _{OUT}
23	---	---	---	---	---	R2 _{IN}	R2 _{IN}	R3 _{IN}	T2 _{IN}
24	---	---	---	---	---	T4 _{OUT}	T4 _{OUT}	T3 _{OUT}	T1 _{IN}

FIGURE 1. Terminal connections.

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4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
 - (2) $T_A = +125^{\circ}\text{C}$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, 6, 7, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
 - (2) $T_A = +125^{\circ}\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*, 2, 3, 9, 10, 11
Group A test requirements (method 5005)	1, 2, 3, 9, 10, 11
Groups C and D end-point electrical parameters (method 5005)	1

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal, or email communication.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0591.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DLA Land and Maritime-VA.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 22-08-15

Approved sources of supply for SMD 5962-89877 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime-VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <https://landandmaritimeapps.dla.mil/programs/smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-8987701XA	<u>3/</u>	AS232F16/883C
5962-8987701EA	1ES66	MAX232MJE/883B
5962-89877012C	1ES66	MAX232MLP/883B
5962-8987702RA	<u>3/</u>	MAX230MJP/883B
5962-8987703CA	<u>3/</u>	MAX231MJD/883B
5962-8987704EA	<u>3/</u>	MAX234MJE/883B
5962-8987705JA	<u>3/</u>	MAX236MRG/883B
5962-8987706JA	<u>3/</u>	MAX237MRG/883B
5962-8987707JA	<u>3/</u>	MAX238MRG/883B
5962-8987708JA	<u>3/</u>	MAX239MRG/883B

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply.

Vendor CAGE
number

1ES66

Vendor name
and address

Maxim Integrated
160 Rio Robles
San Jose, CA 95134

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