

INCH-POUND

MIL-PRF-83532G
SUPPLEMENT 1
15 November 2023

PERFORMANCE SPECIFICATION

DELAY LINES, ACTIVE, GENERAL SPECIFICATION FOR

This supplement forms a part of
[MIL-PRF-83532G](#), dated 15 November 2023.

SPECIFICATION SHEETS

- | | | |
|---------------------------------|---|---|
| MIL-PRF-83532/1 | - | Delay Lines, 14-Pin DIP Compatible, 5 Tap. |
| MIL-PRF-83532/2 | - | Delay Lines, 14-Pin DIP Compatible, 5 Tap. |
| MIL-PRF-83532/3 | - | Delay Lines, 14-Pin DIP Compatible, 10 Tap. |
| MIL-PRF-83532/4 | - | Delay Lines, 14-Pin DIP Compatible, 3 Delay Circuits Per Package. |
| MIL-PRF-83532/7 | - | Delay Lines, Active, 16-Pin Compatible, Programmable 3-Bit,
TTL Compatible, With Input Buffer. |

Custodians:

Army - CR
Navy - EC
Air Force - 85
DLA - CC

Preparing activity:
DLA - CC

Review activities:

Navy - AS, CG, MC, SH
Air Force - 19

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <https://assist.dla.mil>.

AMSC N/A

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