

The documentation and process conversion measures necessary to comply with this document shall be completed by 21 June 2025.

INCH-POUND

MIL-PRF-19500/738C
w/AMENDMENT 1
21 March 2025
SUPERSEDING
MIL-PRF-19500/738C
17 April 2020

PERFORMANCE SPECIFICATION SHEET

TRANSISTOR, NPN, SILICON, POWER DARLINGTON
TYPES 2N7575, 2N7576, AND 2N7577,
QUALITY LEVELS JAN, JANTX, JANTXV, AND JANS

This specification is approved for use by all Departments
and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of
this specification sheet and [MIL-PRF-19500](#).

1. SCOPE

1.1 Scope. This specification covers the performance requirements for NPN silicon, power Darlington transistors. Four levels of product assurance (JAN, JANTX, JANTXV, and JANS) are provided for each device type as specified in [MIL-PRF-19500](#).

1.2 Package outline. The device package style is a three terminal flange mount header configuration (modified TO-254AA) in accordance with [figure 1](#).

1.3 Maximum ratings. Unless otherwise specified, $T_c = +25\text{ }^{\circ}\text{C}$.

Types	P_T		$R_{\theta JC}$ (2)	V_{CBO} and V_{CEX}	V_{CEO}	V_{EBO}	I_B	I_C	T_J and T_{STG}
	$T_A = +25\text{ }^{\circ}\text{C}$	$T_C = +25\text{ }^{\circ}\text{C}$ (1)							
	<u>W</u>	<u>W</u>	<u>$^{\circ}\text{C/W}$</u>	<u>V dc</u>	<u>V dc</u>	<u>V dc</u>	<u>A dc</u>	<u>A dc</u>	<u>$^{\circ}\text{C}$</u>
2N7575	6	175	1.0	350	300	4.0	2.5	20	-65 to +200
2N7576	6	175	1.0	400	350	4.0	2.5	20	-65 to +200
2N7577	6	175	1.0	450	400	4.0	2.5	20	-65 to +200

(1) See [figure 2](#) for temperature-power derating curves.

(2) See [figure 3](#) for thermal impedance graph.

Comments, suggestions, or questions on this document should be addressed to DLA Land and Maritime, ATTN: VAC, P.O. Box 3990, Columbus, OH 43218-3990, or emailed to Semiconductor@dlamail. Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at <https://assist.dla.mil>.

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MIL-PRF-19500/738C
w/AMENDMENT 1

1.4 Primary electrical characteristics. Unless otherwise specified, $T_C = +25\text{ }^{\circ}\text{C}$.

Limits	h_{FE1} (1) $V_{CE} = 5\text{ V dc}$ $I_C = 10\text{ A dc}$	h_{FE2} (1) $V_{CE} = 5\text{ V dc}$ $I_C = 20\text{ A dc}$	$V_{BE(sat)1}$ $I_C = 15\text{ A dc}$ $I_B = 1.2\text{ A dc}$	$V_{CE(sat)1}$ $I_C = 15\text{ A dc}$ $I_B = 1.2\text{ A dc}$
Min	75	50	V dc	V dc
Max	500		2.6	1.5

Limits	$ h_{fe} $	C_{obo}	Pulse response (2)			
	$V_{CE} = 10\text{ V dc}$ $I_C = 1\text{ A dc}$ $f = 1\text{ MHz}$	$V_{CB} = 10\text{ V dc}$ $I_E = 0$ $100\text{ KHz} < f < 1\text{ MHz}$	t_d	t_r	t_s	t_f
Min	25	pF	μs	μs	μs	μs
Max		375	0.1	0.3	1.2	0.3

(1) Pulsed (see 4.5.1).

(2) See figure 4 for pulse response circuit.

1.5 Part or Identifying Number (PIN). The PIN is in accordance with, and as specified herein. See 6.4 for PIN construction example and 6.5 for a list of available PINs.

1.5.1 JAN certification mark and quality level for encapsulated devices. The quality level designators for encapsulated devices that are applicable for this specification sheet from the lowest to the highest level are "JAN", "JANTX", "JANTXV" and "JANS".

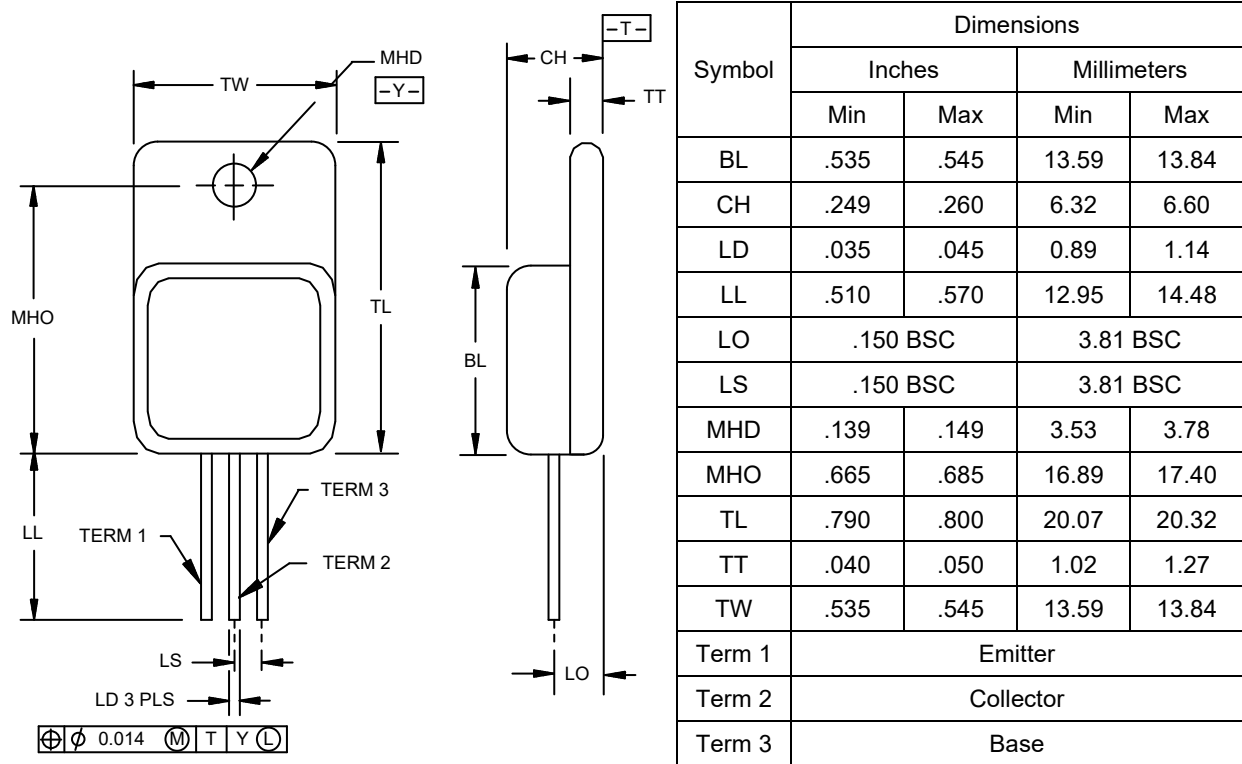
1.5.2 Device type. The designation system for the device types of transistors covered by this specification sheet are as follows.

1.5.2.1 First number and first letter symbols. The transistors of this specification sheet use the first number and letter symbols "2N".

1.5.2.2 Second number symbols. The second number symbols for the transistors covered by this specification sheet are "7575", "7576", and "7577".

1.5.3 Lead finish. The lead finishes applicable to this specification sheet are listed on QML-19500.

MIL-PRF-19500/738C
w/AMENDMENT 1



NOTES:

1. Dimensions are in inches. Millimeters are given for general information only.
2. Protrusion of ceramic eyelets included in dimension LL.
3. All terminals shall be isolated from case.

* FIGURE 1. Dimensions and configuration of three terminal flange mount header package (modified TO-254AA).

2. APPLICABLE DOCUMENTS

2.1 General. The documents listed in this section are specified in sections 3 and 4 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements of documents cited in sections 3 and 4 of this specification, whether or not they are listed.

2.2 Government documents.

2.2.1 Specifications, standards, and handbooks. The following specifications, standards, and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS

MIL-PRF-19500 – Semiconductor Devices, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-750 – Test Methods for Semiconductor Devices.

(Copies of these documents are available online at <https://quicksearch.dla.mil>.)

2.3 Order of precedence. Unless otherwise noted herein or in the contract, in the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 General. The individual item requirements shall be as specified in MIL-PRF-19500 and as modified herein.

3.2 Qualification. Devices furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturer's list (QML-19500) before contract award (see 4.2 and 6.3).

3.3 Acronyms, symbols, and definitions. The acronyms, symbols, and definitions used herein shall be as specified in MIL-PRF-19500.

I_H	–	The collector current applied to the device under test during the heating period.
I_M	–	The measurement current applied to forward bias the junction for measurement of V_{BE} .
t_H	–	The duration of the applied heating power pulse.
t_{SW}	–	Sample window time during which final V_{BE} measurement is made.

3.4 Interface requirements and physical dimensions. The Interface requirements and physical dimensions shall be as specified in MIL-PRF-19500 and on figure 1 herein.

3.4.1 Lead finish. The lead finish shall be solderable in accordance with MIL-STD-750, MIL-PRF-19500, and herein. Where a choice of lead finish is desired, it shall be specified in the acquisition document (see 6.2).

3.4.2 Lead formation. Where a choice of lead formation is desired, it shall be specified in the acquisition document (see 6.2). When lead formation is performed, as a minimum, the vendor shall perform hermetic seal in accordance with screen 14 of table E-IV of MIL-PRF-19500 and 100 percent dc testing in accordance with table I, subgroup 2 herein on all devices that had its leads altered.

MIL-PRF-19500/738C
w/AMENDMENT 1

3.4.3 Lead isolation. Methods used for electrical isolation of the terminal feedthroughs shall employ material that contain a minimum of 90 percent ceramic Al_2O_3 or equivalent. Examples of such construction techniques are metalized eyelets or ceramic walled packages.

3.4.4 Polarity. The identification of terminals of the device package shall be as shown on [figure 1](#).

3.5 Marking. Marking shall be in accordance with [MIL-PRF-19500](#).

3.6 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in [1.3](#), [1.4](#), and [table I](#) herein.

3.7 Electrical test requirements. The electrical test requirements shall be as specified in [table I](#).

3.8 Workmanship. Semiconductor devices shall be processed in such a manner as to be uniform in quality and shall be free from other defects that will affect life, serviceability, or appearance.

4. VERIFICATION

4.1 Classification of inspections. The inspection requirements specified herein are classified as follows:

- a. Qualification inspection (see 4.2).
- b. Screening (see [4.3](#)).
- c. Conformance inspection (see [4.4](#) and [tables I and II](#)).

4.2 Qualification inspection. Qualification inspection shall be in accordance with [MIL-PRF-19500](#), and as specified herein.

4.2.1 Group E inspection. Group E inspection shall be performed for qualification or re-qualification only. In case qualification was awarded to a prior revision of the specification sheet that did not request the performance of [table II](#) tests, the tests specified in [table II](#) herein that were not performed in the prior revision shall be performed on the first inspection lot of this revision to maintain qualification.

4.3 Screening (quality levels JANS, JANTXV, and JANTX only). Screening shall be in accordance with table E-IV of [MIL-PRF-19500](#), and as specified herein. The following measurements shall be made in accordance with [table I](#) herein. Devices that exceed the limits of [table I](#) herein shall not be acceptable.

Screen	Measurements for JANS	Measurements for JANTX and JANTXV
3c (1)	Thermal impedance (see 4.3.1)	Thermal impedance (see 4.3.1)
9	I_{CEX1}	I_{CEX1}
11	I_{CEX1} and h_{FE2} , ΔI_{CEX1} = 100 percent of initial value or 50 nA dc, whichever is greater.	I_{CEX1} and h_{FE2} , ΔI_{CEX1} = 100 percent of initial value or 100 nA dc, whichever is greater.
12	See 4.3.2	See 4.3.2
13	Subgroups 2 and 3 of table I herein. ΔI_{CEX1} = 100 percent of initial value or 50 nA dc, whichever is greater. Δh_{FE2} = ± 15 percent of initial value.	Subgroup 2 of table I herein. ΔI_{CEX1} = 100 percent of initial value or 100 nA dc, whichever is greater. Δh_{FE2} = ± 25 percent of initial value.
17	Dielectric withstanding voltage (see 4.3.3).	Dielectric withstanding voltage (see 4.3.3).

- (1) Shall be performed anytime after temperature cycling, screen 3a; Quality levels JANTX and JANTXV levels do not need to be repeated in screening requirements.

MIL-PRF-19500/738C
w/AMENDMENT 1

4.3.1 Thermal impedance. The thermal impedance measurements shall be performed in accordance with test method 3131 of [MIL-STD-750](#) using the guidelines in that test method for determining I_M , I_H , t_H , t_{SW} , t_{MD} (and V_H where appropriate). The thermal impedance limit used in 4.3, screen 3c, and [table I](#), subgroup 2 herein shall be set statistically by the supplier. See group E inspection ([table II](#), subgroup 4) herein.

4.3.2 Power burn-in conditions. Power burn-in conditions shall be as follows: $T_J = +175^\circ\text{C}$ minimum, V_{CB} greater than or equal to 25 V dc; $T_A = +30^\circ\text{C}$ maximum.

4.3.3 Dielectric withstanding voltage (for TO-254 packages only). The test for dielectric withstanding voltage shall be performed in accordance with method 1081 of [MIL-STD-750](#). After the test, electrical end-points in accordance with subgroup 2 of [table I](#) herein shall be taken. The following details shall apply:

- a. Magnitude of test voltage.....900 V dc.
- b. Duration of application of test voltage.....15 seconds (min).
- c. Points of application of test voltage.....All terminals to case (bunch connection).
- d. Method of connection.....Mechanical.
- e. Kilovolt-ampere rating of high voltage source.....1,200 V/1.0 mA (min).
- f. Maximum leakage current.....1.0 mA.
- g. Voltage ramp up time.....500 V/second.

4.4 Conformance inspection. Conformance inspection shall be in accordance with [MIL-PRF-19500](#), and as specified herein. If alternate screening is being performed in accordance with [MIL-PRF-19500](#), a sample of screened devices shall be submitted to and pass the requirements of [table I](#), subgroups 1 and 2. Group B inspection in accordance with subgroup 1 of table E-VIB of [MIL-PRF-19500](#) is not required to be performed again if group B has already been satisfied in accordance with [4.4.2](#).

4.4.1 Group A inspection. Group A inspection shall be conducted in accordance with table E-V of [MIL-PRF-19500](#) and [table I](#) herein. Electrical end-point measurements shall be in accordance with [table I](#), subgroup 2 herein.

4.4.2 Group B inspection. Group B inspection shall be conducted in accordance with the tests and conditions specified for subgroup testing in 4.4.2.1 for quality level JANS and 4.4.2.2 for quality levels JAN, JANTX, and JANTXV. Delta requirements shall be in accordance with the applicable step of [4.6](#) herein.

4.4.2.1 Quality level JANS (table E-VIA of [MIL-PRF-19500](#)).

<u>Subgroup</u>	<u>Method</u>	<u>Conditions</u>
B3	1037	For solder die attach: V_{CB} greater than or equal to 100 V dc.
B4	1027	For eutectic die attach: V_{CB} greater than or equal to 100 V dc; adjust P_T to achieve $T_J = +175^\circ\text{C}$ minimum; $T_A = +30^\circ\text{C}$ maximum.

4.4.2.2 Quality level levels JAN, JANTX, and JANTXV (table E-VIB of [MIL-PRF-19500](#)).

<u>Subgroup</u>	<u>Method</u>	<u>Conditions</u>
B3	1027	For eutectic die attach: V_{CB} greater than or equal to 100 V dc; adjust P_T to achieve $T_J = +175^\circ\text{C}$ minimum; $T_A = +30^\circ\text{C}$ maximum.
B3	1037	For solder die attach: V_{CB} greater than or equal to 100 V dc.

MIL-PRF-19500/738C
w/AMENDMENT 1

4.4.3 Group C inspection. Group C inspection shall be conducted in accordance with the tests and conditions specified for subgroup testing in table E-VII of [MIL-PRF-19500](#) and as follows herein. Delta measurements shall be in accordance with the applicable step of [4.6](#) herein.

<u>Subgroup</u>	<u>Method</u>	<u>Conditions</u>
C2	2036	Test condition A; weight = 10 pounds (4.54 Kg); time = 15 seconds.
C5	3131	See 4.3.1 , $R_{\theta JC} = 1.0\text{ }^{\circ}\text{C/W}$.
C6	1027	For eutectic die attach: V_{CB} greater than or equal to 100 V dc; adjust P_T to achieve $T_J = +175\text{ }^{\circ}\text{C}$ minimum; $T_A = +30\text{ }^{\circ}\text{C}$ maximum, 1,000 hours minimum.
C6	1037	For solder die attach: V_{CB} greater than or equal to 100 V dc.

4.4.4 Group E inspection. Group E inspection shall be conducted in accordance with the conditions specified for subgroup testing in table E-IX of [MIL-PRF-19500](#) and as specified in [table II](#) herein. Delta measurements shall be in accordance with the applicable step of [4.6](#) herein.

4.5 Method of inspection. Methods of inspection shall be as specified in the appropriate tables and as follows.

4.5.1 Pulse response measurements. The conditions for pulse response measurement shall be as specified in section 4 of [MIL-STD-750](#).

4.5.2 Insulation resistance test. The isolation resistance test shall be in accordance with test method 1016 of [MIL-STD-750](#). The test voltage shall be 1000 volts. The following preparations shall apply: short the collector, emitter and base terminals together. The limit for acceptance shall be $10^9\text{ }\Omega$ minimum.

MIL-PRF-19500/738C
w/AMENDMENT 1

4.6 Delta measurements. The requirements for delta measurements for groups B, C, and E shall be as specified below.

Steps	Inspection (1) (2) (3) (4) (5)	MIL-STD-750		Symbol	Limits		Unit
		Method	Conditions		Min	Max	
1	Collector to base cutoff current 2N7575 2N7576 2N7577	3036	Bias condition D $V_{CB} = 350 \text{ V dc}$ $V_{CB} = 400 \text{ V dc}$ $V_{CB} = 450 \text{ V dc}$	ΔI_{CBO}	100 percent of initial value or 100 nA, whichever is greater.		
2	Forward – current transfer ratio	3076	$V_{CE} = 5 \text{ V dc}$; $I_C = 20 \text{ A dc}$; pulsed (see 4.5.1)	Δh_{FE2}	± 25 percent change from initial reading.		
3	Saturation voltage, collector to emitter	3071	$I_C = 15 \text{ A dc}$; $I_B = 1.2 \text{ A dc}$, pulsed (see 4.5.1)	$\Delta V_{CE(sat)1}$	$\pm 200 \text{ mV}$ change from previously measured value.		

- (1) Devices which exceed the group A limits for this test shall not be acceptable.
- (2) The delta electrical measurements for group B, product assurance level JANS, shall be as follows:
 - a. In addition to the measurements specified for subgroup 4 of table E-VIA of MIL-PRF-19500, the measurements of steps 1 and 2 of this table shall also be taken.
 - b. In addition to the measurements specified for subgroup 5 of table E-VIA of MIL-PRF-19500, the measurements of steps 1 and 2 of this table shall also be taken.
- (3) The delta electrical measurements for group B, product assurance levels JAN, JANTX and JANTXV, shall be as follows:
 - a. In addition to the measurements specified for subgroup 3 of table E-VIB of MIL-PRF-19500, the measurements of steps 1 and 2 of this table shall also be taken.
 - b. In addition to the measurements specified for subgroup 6 of table E-VIB of MIL-PRF-19500, the measurements of step 2 of this table shall also be taken.
- (4) The delta electrical measurements for group C, all product assurance levels, shall be as follows:
 - a. In addition to the measurements specified for subgroup 2 of table E-VII of MIL-PRF-19500, the measurements of step 1 of this table shall also be taken.
 - b. In addition to the measurements specified for subgroup 3 of table E-VII of MIL-PRF-19500, the measurements of step 1 of this table shall also be taken.
 - c. In addition to the measurements specified for subgroup 6 of table E-VII of MIL-PRF-19500, the measurements of steps 1, 2, and 3 of this table shall also be taken.
- (5) The delta measurements for group E, all product assurance levels, shall be as follows: In addition to the measurements specified for subgroups 1 and 2 of table E-IX of MIL-PRF-19500, the measurements of steps 1, 2, and 3 of this table shall also be taken.

MIL-PRF-19500/738C
w/AMENDMENT 1

TABLE I. Group A inspection.

Inspection <u>1/</u>	MIL-STD-750		Symbol	Limit		Unit
	Method	Conditions		Min	Max	
<u>Subgroup 1</u>						
Visual and mechanical examination	2071					
<u>Subgroup 2</u>						
Thermal impedance <u>2/</u>	3131	See 4.3.1	$Z_{\theta JX}$			°C/W
Breakdown voltage, collector to emitter	3011	Bias condition D, $I_C = 200$ mA dc; pulsed (see 4.5.1)	$V_{(BR)CEO}$	300		V dc
2N7575				350		V dc
2N7576				400		V dc
2N7577						
Collector to emitter cutoff current	3041	Bias condition A, $V_{BE} = -1.5$ V dc	I_{CEX1}			
2N7575		$V_{CE} = 350$ V dc			0.1	mA dc
2N7576		$V_{CE} = 400$ V dc			0.1	mA dc
2N7577		$V_{CE} = 450$ V dc			0.1	mA dc
Collector to emitter cutoff current	3041	Bias Condition D	I_{CEO}			
2N7575		$V_{CE} = 300$ V dc			0.1	mA dc
2N7576		$V_{CE} = 350$ V dc			0.1	mA dc
2N7577		$V_{CE} = 400$ V dc			0.1	mA dc
Collector to base cutoff current	3036	Bias condition D	I_{CBO}			
2N7575		$V_{CBO} = 350$ V dc			0.1	mA dc
2N7576		$V_{CBO} = 400$ V dc			0.1	mA dc
2N7577		$V_{CBO} = 450$ V dc			0.1	mA dc
Emitter to base cutoff current	3061	Bias condition D, $V_{EB} = 4$ V dc	I_{EBO}		300	mA dc
Base emitter voltage (saturated)	3066	Test condition A; $I_C = 15$ A dc; pulsed (see 4.5.1); $I_B = 1.2$ A dc	$V_{BE(SAT)}$		2.6	V dc
Saturation voltage; collector to emitter	3071	$I_C = 15$ A dc; pulsed (see 4.5.1) $I_B = 1.2$ A dc	$V_{CE(SAT)1}$		1.5	V dc
Saturation voltage; collector to emitter	3071	$I_C = 20$ A dc; pulsed (see 4.5.1) $I_B = 1.2$ A dc	$V_{CE(SAT)2}$		1.7	V dc
Forward-current transfer ratio	3076	$V_{CE} = 5$ V dc; $I_C = 10$ A dc; pulsed (see 4.5.1)	h_{FE1}	75	500	
Forward-current transfer ratio	3076	$V_{CE} = 5$ V dc; $I_C = 20$ A dc; pulsed (see 4.5.1)	h_{FE2}	50		

See footnotes at end of table.

MIL-PRF-19500/738C
w/AMENDMENT 1

TABLE I. Group A inspection – Continued.

Inspection <u>1/</u>	MIL-STD-750		Symbol	Limit	Limit	Unit
	Method	Conditions		Min	Max	
<u>Subgroup 3</u>						
High-temperature operation:		T _A = +150 °C				
Collector to emitter cutoff current	3041	Bias condition A; V _{BE} = -1.5 V dc	I _{CEX2}			
2N7575		V _{CE} = 350 V dc			0.1	mA dc
2N7576		V _{CE} = 400 V dc			0.1	mA dc
2N7577		V _{CE} = 450 V dc			0.1	mA dc
Low-temperature operation:		T _A = -55 °C				
Forward-current transfer ratio	3076	V _{CE} = 5 V dc, I _C = 10 A dc; pulsed (see 4.5.1)	h _{FE3}	20		
<u>Subgroup 4</u>						
Magnitude of common emitter small-signal short-circuit forward- current transfer ratio	3306	V _{CE} = 10 V dc; I _C = 1 A dc; f = 1 MHz	h _{fe}	25		
Open circuit output capacitance	3236	V _{CB} = 10 V dc; I _E = 0; 100 kHz ≤ f ≤ 1.0 MHz	C _{obo}	150	375	pF
Pulse response:	3251	Test condition A except test circuit and pulse requirements in accordance with figure 4, T _A = +25 °C, V _{CC} = 300V, I _C = 10A, I _{B1} = 0.5A, V _{BE(off)} = -5V, t _p = 50 μs				
Pulse delay time			t _d		0.1	μs
Pulse rise time			t _r		0.3	μs
Pulse storage time			t _s		1.2	μs
Pulse fall time			t _f		0.3	μs
<u>Subgroup 5</u>						
Safe operating area (continuous dc)	3051	T _C = +25 °C; t = 1 s; 1 cycle; see figure 5				
<u>Test 1</u> (All device types)						
		V _{CE} = 8.0 V dc; I _C = 20 A dc				

See footnotes at end of table.

MIL-PRF-19500/738C
w/AMENDMENT 1

TABLE I. Group A inspection – Continued.

Inspection <u>1/</u>	MIL-STD-750		Symbol	Limit	Limit	Unit
	Method	Conditions		Min	Max	
<u>Subgroup 5</u> – Continued.						
<u>Test 2</u> (All device types)		$V_{CE} = 30 \text{ V dc}; I_C = 3.0 \text{ A dc}$				
<u>Test 3</u> (All device types)		$V_{CE} = 100 \text{ V dc}; I_C = 0.17 \text{ A dc}$				
<u>Test 4</u> 2N7575 2N7576 2N7577		$V_{CE} = 300 \text{ V dc}; I_C = 25 \text{ mA dc}$ $V_{CE} = 350 \text{ V dc}; I_C = 19 \text{ mA dc}$ $V_{CE} = 400 \text{ V dc}; I_C = 15 \text{ mA dc}$				
Safe operating area (switching); inductive collector to emitter sustaining voltage 2N7575 2N7576 2N7577	3053	$T_A = +25^\circ\text{C}, I_C = 2 \text{ A}, I_{B1} = 0.1 \text{ A},$ $I_{B2} = 0 \text{ A}, L = 1 \text{ mH},$ clamped at rated V_{CEO}	$V_{CEO(sus)}$			
		Clamp voltage = 300 V dc		300		V dc
		Clamp voltage = 350 V dc		350		V dc
		Clamp voltage = 400 V dc		400		V dc
Safe operating area (switching); inductive collector to emitter sustaining voltage 2N7575 2N7576 2N7577	3053	$T_A = +25^\circ\text{C}, I_C = 2 \text{ A}, I_{B1} = 0.5 \text{ A},$ $V_{BE(off)} = 1 \text{ V}, L = 1 \text{ mH},$ clamped at rated V_{CEO}	$V_{CEX(sus)}$			
		Clamp voltage = 300 V dc		300		V dc
		Clamp voltage = 350 V dc		350		V dc
		Clamp voltage = 400 V dc		400		V dc
<u>Subgroup 6</u>						
Not applicable						
<u>Subgroup 7</u>						
Insulation resistance test	1016	See 4.5.2.	R_{iso}	10^9		Ω

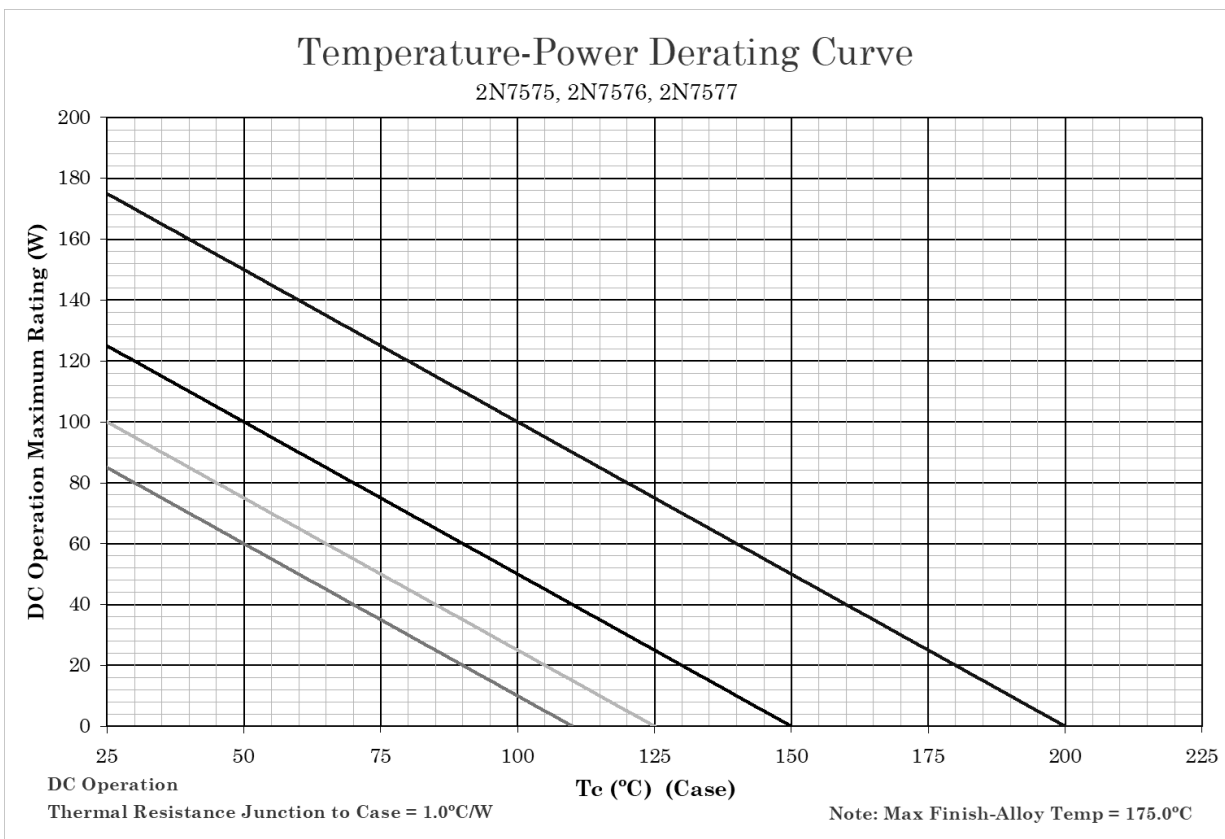
1/ For sampling plan see MIL-PRF-19500.

2/ This test required for the following end-point measurements only:
Group B, subgroups 3, 4, and 5 (JANS).
Group B, subgroups 2 and 3 (JAN, JANTX, and JANTXV).
Group C, subgroup 2 and 6.
Group E, subgroup 1.

MIL-PRF-19500/738C
w/AMENDMENT 1

TABLE II. Group E inspection (all quality levels) – for qualification and re-qualification only.

Inspection	MIL-STD-750		Sample plan
	Method	Conditions	
<u>Subgroup 1</u>			45 devices, c = 0
Thermal shock (liquid to liquid)	1056	0°C to +100°C, 100 cycles	
Hermetic seal	1071	Fine and gross leak.	
Electrical measurements		See table I , subgroup 2 and 4.6 herein.	
<u>Subgroup 2</u>			45 devices, c = 0
Blocking life	1048	1,000 hours minimum, T _A = +150°C, V _{CB} = 80 percent of rated.	
Electrical measurements		See table I , subgroup 2 and 4.6 herein.	
<u>Subgroup 4</u>			Sample size N/A
Thermal impedance curves		See MIL-PRF-19500 .	
<u>Subgroup 5</u>			15 devices, c = 0
Barometric pressure (reduced)	1001	Condition C. See 1.3 .	
<u>Subgroup 6</u>			
Electrostatic discharge sensitivity	1020		
<u>Subgroup 8</u>			45 devices, c = 0
Reverse voltage leakage stability	1033	Condition A for all devices.	

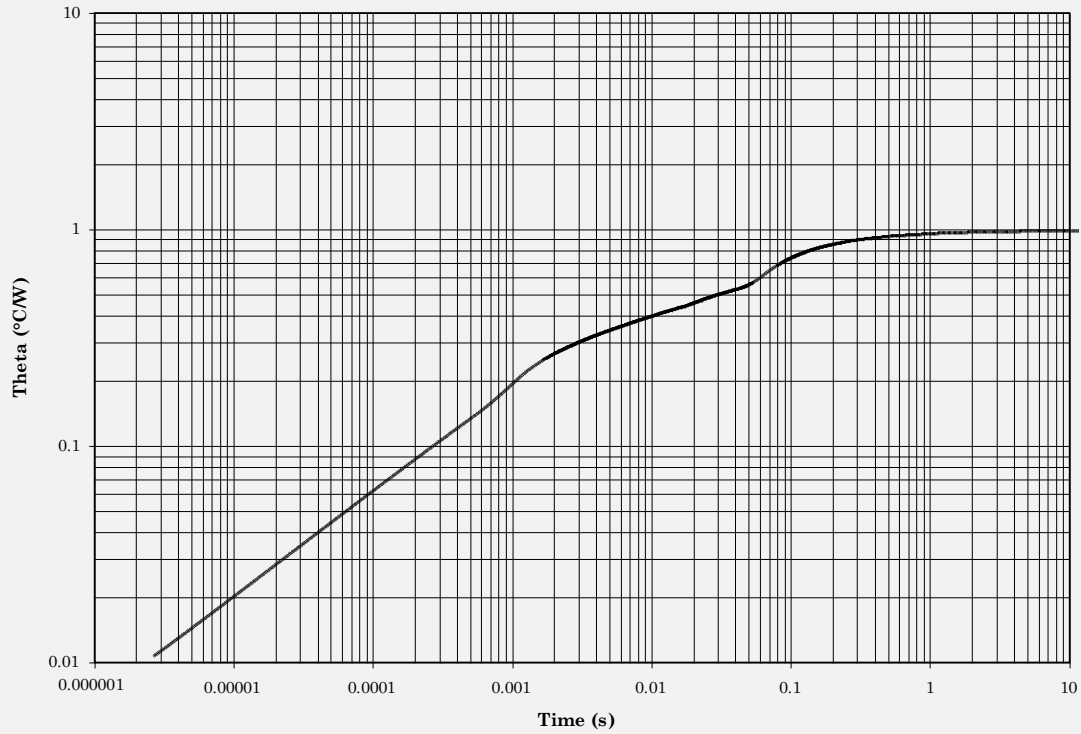


NOTES:

1. All devices are capable of operating at $\leq T_J$ specified on this curve. Any parallel line to this curve will intersect the appropriate power for the desired maximum T_J allowed.
2. Derate design curve constrained by the maximum junction temperature ($T_J \leq +200\text{ }^\circ\text{C}$) and power rating specified (see 1.3 herein).
3. Derate design curve chosen at $T_J \leq +150\text{ }^\circ\text{C}$, where the maximum temperature of electrical test is performed.
4. Derate design curves chosen at $T_J \leq +125\text{ }^\circ\text{C}$, and $+110\text{ }^\circ\text{C}$ to show power rating where most users want to limit T_J in their application.

FIGURE 2. Temperature-power derating graph.

Maximum Thermal Impedance
TO-254AA package, $T_C = +25^\circ\text{C}$



$R_{\theta JC} = 1.0 \text{ } ^\circ\text{C/W max.}$

FIGURE 3. Thermal impedance graph.

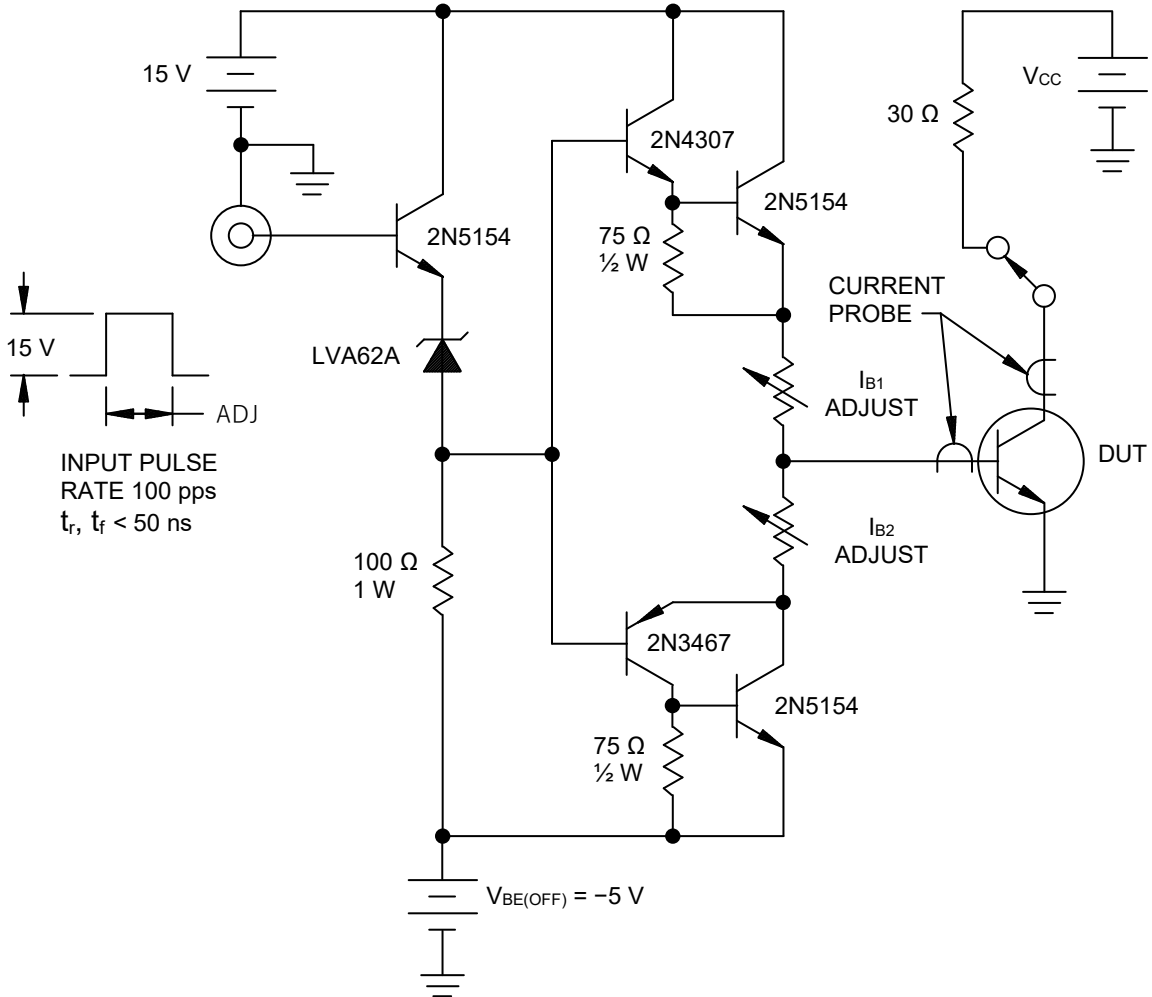


FIGURE 4. Pulse response test circuit.

MIL-PRF-19500/738C
w/AMENDMENT 1

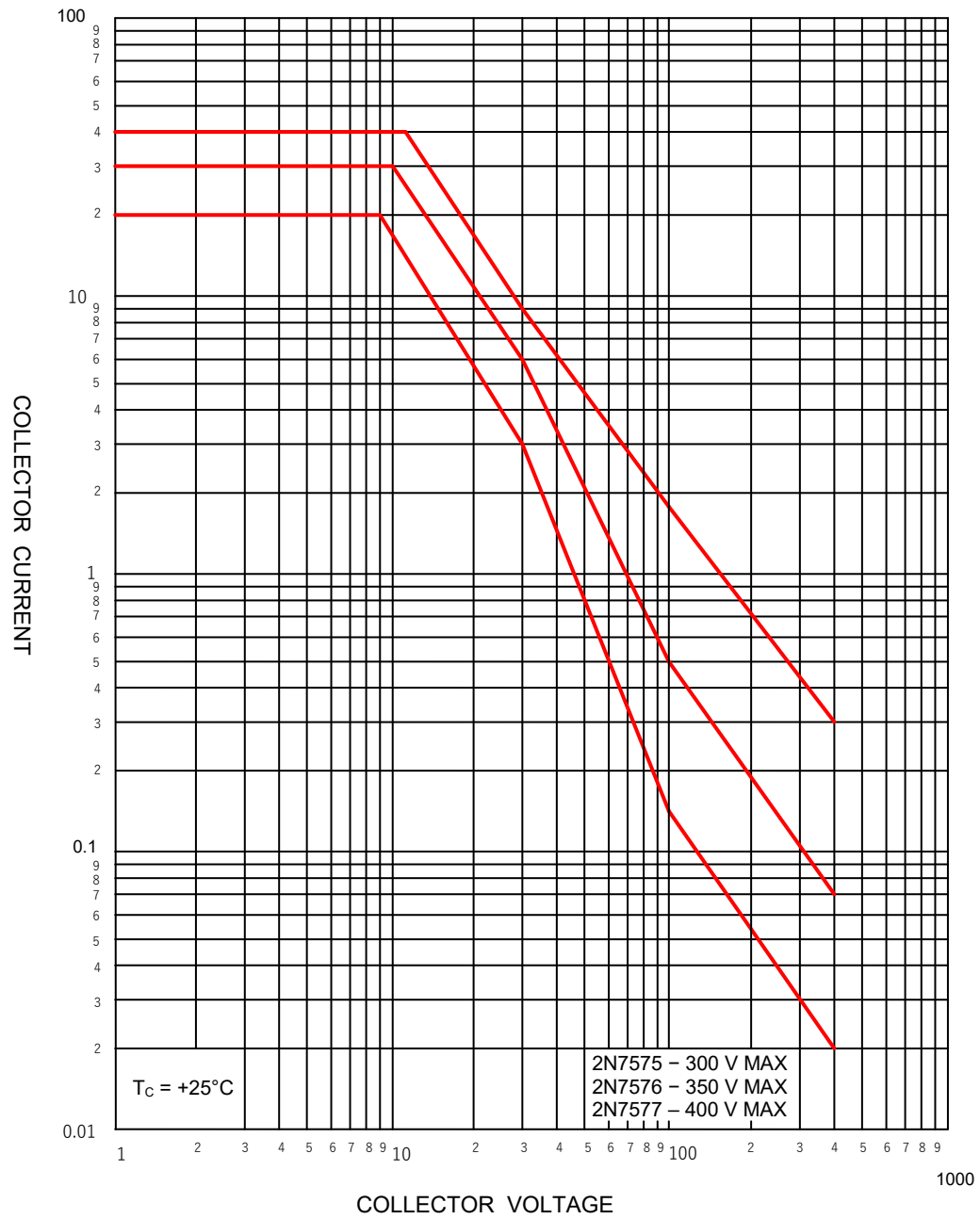


FIGURE 5. Maximum safe operating area graph.

MIL-PRF-19500/738C
w/AMENDMENT 1

5. PACKAGING

5.1 Packaging. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When packaging of materiel is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activities within the Military Service or Defense Agency, or within the Military Service's system commands. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory. The notes specified in [MIL-PRF-19500](#) are applicable to this specification.)

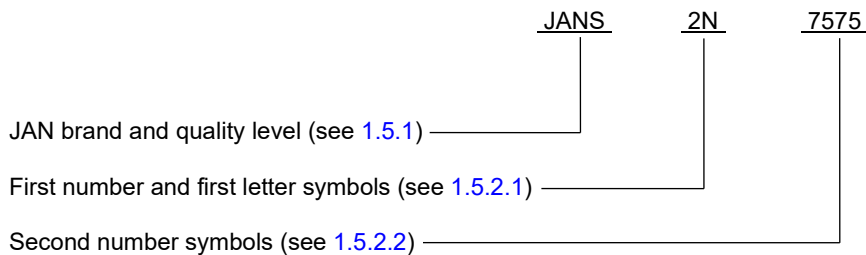
6.1 Intended use. Semiconductors conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.2 Acquisition requirements. Acquisition documents should specify the following:

- a. Title, number, and date of this specification.
- b. Packaging requirements (see 5.1).
- c. Lead finish (see [3.4.1](#)) and if applicable, any needed lead formation (see [3.4.2](#)).
- d. The complete PIN, see [1.5](#) and [6.5](#).

6.3 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List ([QML-19500](#)) whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DLA Land and Maritime, ATTN: VQE, P.O. Box 3990, Columbus, OH 43218-3990 or e-mail vqe.chief@dla.mil. An online listing of products qualified to this specification may be found in the Qualified Products Database (QPD) at <https://qpldocs.dla.mil>.

6.4 PIN construction example. The PINs for encapsulated devices are constructed using the following form:



MIL-PRF-19500/738C
w/AMENDMENT 1

6.5 List of PINs. The following is a list of possible PINs available on this specification sheet.

Base quality level	Quality level "TX"	Quality level "TXV"	Quality level "S"
JAN2N7575	JANTX2N7575	JANTXV2N7575	JANS2N7575
JAN2N7576	JANTX2N7576	JANTXV2N7576	JANS2N7576
JAN2N7577	JANTX2N7577	JANTXV2N7577	JANS2N7577

6.6 Request for new types and configurations. Requests for new device types or configurations for inclusions in this specification sheet should be submitted to: DLA Land and Maritime, ATTN: VAC, Post Office Box 3990, Columbus, OH 43218-3990 or by electronic mail at Semiconductor@dla.mil or by facsimile (614) 692-6939 or DSN 850-6939.

* 6.7 Amendment notations. The margins of this specification are marked with asterisks to indicate modifications generated by this amendment. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous issue.

Custodians:
Army – CR
* Navy – SH
Air Force – 85
NASA – NA
DLA – CC

Preparing activity:
DLA – CC

(Project 5961-2025-029)

Review activities:
Army – MI
Air Force – 19

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <https://assist.dla.mil>.