

INCH-POUND

MIL-M-38510/314C

14 July 2003

SUPERSEDING

MIL-M-38510/314B

23 May 1978

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, LOW-POWER SCHOTTKY, TTL, MONOSTABLE MULTIVIBRATORS, MONOLITHIC SILICON

Inactive for new design after 18 April 1997.

This specification is approved for use by all Departments
and Agencies of the Department of Defense.

1. SCOPE

1.1 Scope. This specification covers the detail requirements for monolithic silicon, low-power Schottky TTL, monostable multivibrator microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.3).

1.2 Part number. The part number should be in accordance with MIL-PRF-38535, and as specified herein.

1.2.1 Device types. The device types should be as follows:

<u>Device type</u>	<u>Circuit</u>
01	Dual monostable multivibrator, retriggerable, with clear
02	Dual monostable multivibrator, Schmitt trigger inputs, with clear
03	Single monostable multivibrator, retriggerable, with clear

1.2.2 Device class. The device class should be the product assurance level as defined in MIL-PRF-38535.

1.2.3 Case outlines. The case outlines should be as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
A	GDFP5-F14 or CDFP6-F14	14	Flat pack
B	GDFP4-14	14	Flat pack
C	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
E	GDIP1-T16 or CDIP2-T16	16	Dual-in-line
F	GDFP2-F16 or CDFP3-F16	16	Flat pack
2	CQCC1-N20	20	Square leadless chip carrier

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, P. O. Box 3990, Columbus, OH 43216-5000, by using the self addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

AMSC N/A

FSC 5962

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1.3 Absolute maximum ratings.

Supply voltage range	-0.5 V dc to 7.0 V dc
Input voltage range	-1.5 V dc at -18 mA to 5.5 V dc
Storage temperature range	-65° to +150°C
Maximum power dissipation per flip-flop, (P_D) <u>1/</u>	
Device type 01	110 mW dc
Device type 02	149 mW dc
Device type 03	61 mW dc
Lead temperature (soldering, 10 seconds)	300°C
Thermal resistance, junction to case (θ_{JC}):	
Cases A, B, C, D, E, F, and 2	(See MIL-STD-1835)
Junction temperature (T_J) <u>2/</u>	175°C

1.4 Recommended operating conditions.

Supply voltage (V_{CC})	4.5 V dc minimum to 5.5 V dc maximum
Minimum high level input voltage (V_{IH})	2.0 V dc
Maximum low level input voltage (V_{IL})	0.7 V dc
Case operating temperature range (T_C)	-55° to +125°C
Minimum pulse width	40 ns
Input pulse rise/fall time, device type 02	
Schmitt, B input	1 V/s minimum
Logic, A input	1 V/ μ s minimum
Clear-inactive-state setup time	
Device type 02	15 ns minimum
External timing resistance, R_{ext}	
Device type 01, 03	5 k Ω minimum, 180 k Ω maximum
Device type 02	1.4 k Ω minimum, 70 k Ω maximum
External timing capacitance, C_{ext}	
Device type 01, 03	No restriction
Device type 02	1,000 μ F maximum
Output duty cycle, device type 02	
$R_T = 2$ k Ω	50% duty cycle maximum
$T_T = 70$ k Ω	90 % duty cycle maximum
Wiring capacitance, R_{ext}/C_{ext} terminal	
Device type 01, 03 (referenced to GND)	50 pF maximum

1/ Must withstand the added P_D due to short-circuit test (e.g., I_{OS}).2/ Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening conditions in accordance with MIL-PRF-38535.

2. APPLICABLE DOCUMENTS

2.1 Government documents.

2.1.1 Specifications and Standards. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Departments of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Method Standard for Microelectronics.
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines

(Unless otherwise indicated, copies of the above specifications and standards are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Qualification. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).

3.2 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.3 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.

3.3.1 Terminal connections and logic diagrams. The terminal connections and logic diagrams shall be as specified on figure 1.

3.3.2 Truth table and functional description. The truth table and functional description shall be as specified on figure 2.

3.3.3 Schematic circuits. The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity and the preparing activity upon request.

3.3.4 Case outlines. The case outlines shall be as specified in 1.2.3.

3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).

3.5 Electrical performance characteristics. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.

3.6 Electrical test requirements. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.

3.8 Microcircuit group assignment. The devices covered by this specification shall be in microcircuit group number 10 (see MIL-PRF-38535, appendix A).

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.

4.2 Screening. Screening shall be in accordance with, MIL-PRF-38535 and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:

- a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
- c. Additional screening for space level product shall be as specified in MIL-PRF-38535, appendix B.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.

4.4 Technology Conformance Inspection (TCI). Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection. Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6 shall be omitted.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _C ≤ +125°C unless otherwise specified	Device types	Limits		Unit
				Min	Max	
High level output voltage	V _{OH}	V _{CC} = 4.5 V, V _{IL} = 0.7 V, V _{IH} = 2.0 V, I _{OH} = -400 μA	All	2.5		V
Low level output voltage	V _{OL}	V _{CC} = 4.5 V, V _{IL} = 0.7 V, V _{IH} = 2.0 V, I _{OL} = 4 mA	All		0.4	V
Input clamp voltage	V _{IC}	V _{CC} = 4.5 V, I _{IN} = -18 mA, T _C = +25°C	All		-1.5	V
Low level input current	I _{IL1}	V _{CC} = 5.5 V, V _{IN} = 0.4 V	01, 03	-160	-400	μA
Low level input current at clear input	I _{IL2}		02	-30	-680	
Low level input current at A _{IN}			02	-30	-400	
Low level input current B _{IN}			02	-30	-580	
High level input current	I _{IH1}	V _{CC} = 5.5 V, V _{IN} = 2.7 V	All		20	μA
High level input current	I _{IH2}	V _{CC} = 5.5 V, V _{IN} = 5.5 V	All		100	
Short circuit output current	I _{OS}	V _{CC} = 5.5 V, V _{IH} = 5.5 V, V _{IL} = GND	All	-15	-130	mA
Supply current (quiescent)	I _{CC1}	V _{CC} = 5.5 V	02		11	mA
Supply current (quiescent or triggered)	I _{CC2}		01		20	
			03		11	
Supply current (triggered)	I _{CC3}			02		
Propagation delay time low to high level from input A	t _{PLH1}	V _{CC} = 5.0 V C _L = 50 pF ±5%, R _L = 2 kΩ ±5% C _{ext} = <u>1</u> / R _{ext} = <u>1</u> / 	01, 03	5	57	ns
			02	5	113	
Propagation delay time low to high level from input B	t _{PLH2}		01, 03	5	74	
			02	5	90	
Propagation delay time low to high level from clear	t _{PLH3}		01, 03	5	75	
			02	5	105	
Propagation delay time high to low level from input A	t _{PHL1}		01, 03	5	75	
			02	5	128	

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _C ≤ +125°C unless otherwise specified	Device types	Limits		Unit
				Min	Max	
Propagation delay time high to low level from input B	t _{PHL2}	V _{CC} = 5.0 V C _L = 50 pF ± 10% R _L = 2kΩ ± 5%	01, 03	5	92	ns
			02	5	105	
Propagation delay time high to low level from clear	t _{PHL3}	C _{ext} = <u>1</u> / R _{ext} = <u>1</u> /	01, 03	5	48	ns
			02	5	90	
Minimum pulse width of Q output	t _{P(MIN)}	V _{CC} = 5.0 V C _L = 50 pF ± 10% R _L = 2kΩ ± 5%	01, 03		308	ns
Width of Q output pulse	t _{P1}	C _{ext} = <u>2</u> / ±10% R _{ext} = <u>2</u> / ±10%	02	20	91	
	t _{P2}		02	70	195	
	t _{P3}		02	600	850	
	t _{P4}		01, 03	3.0	6.25	μs
	t _{P5}		02	5.5	8.5	ms

1/ For propagation delay tests, see table III for C_{ext} and R_{ext} values.

2/ t_{P(MIN)} test, C_{ext} = open and R_{ext} = 5 kΩ.

t_{P1} test, C_{ext} = open and R_{ext} = 2 kΩ.

t_{P2} test, C_{ext} = 80 pF and R_{ext} = 2 kΩ.

t_{P3} test, C_{ext} = 100 pF and R_{ext} = 10 kΩ.

t_{P4} test, C_{ext} = 1,000 pF and R_{ext} = 10 kΩ.

t_{P5} test, C_{ext} = 1μF and R_{ext} = 10 kΩ.

TABLE II. Electrical test requirements.

MIL-PRF-38535 test requirements	Subgroups (see table III)	
	Class S devices	Class B devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 7, 9, 10, 11	1*, 2, 3, 7, 9
Group A test requirements	1, 2, 3, 7, 8, 9, 10, 11	1, 2, 3, 7, 8, 9
Group B electrical test parameters when using method 5005 QCI option	1, 2, 3 9, 10, 11	N/A
Group C end-point electrical parameters	1, 2, 3, 9, 10, 11	1, 2, 3
Additional electrical subgroups for group C inspections	N/A	10, 11
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

*PDA applies to subgroup 1.

4.4.2 Group B inspection. Group B inspection shall be in accordance with table II MIL-PRF-38535.

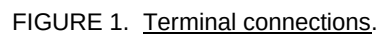
4.4.3 Group C inspection. Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- c. Subgroups 3 and 4 shall be added to group C inspection parameters for class B devices and shall consist of the tests, conditions, and limits specified for subgroups 10 and 11 of group A.

4.4.4 Group D inspection. Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.

4.5 Methods of inspection. Methods of inspection shall be specified and as follows:

4.5.1 Voltage and current. All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.



DEVICE TYPE 02
CASES E AND F

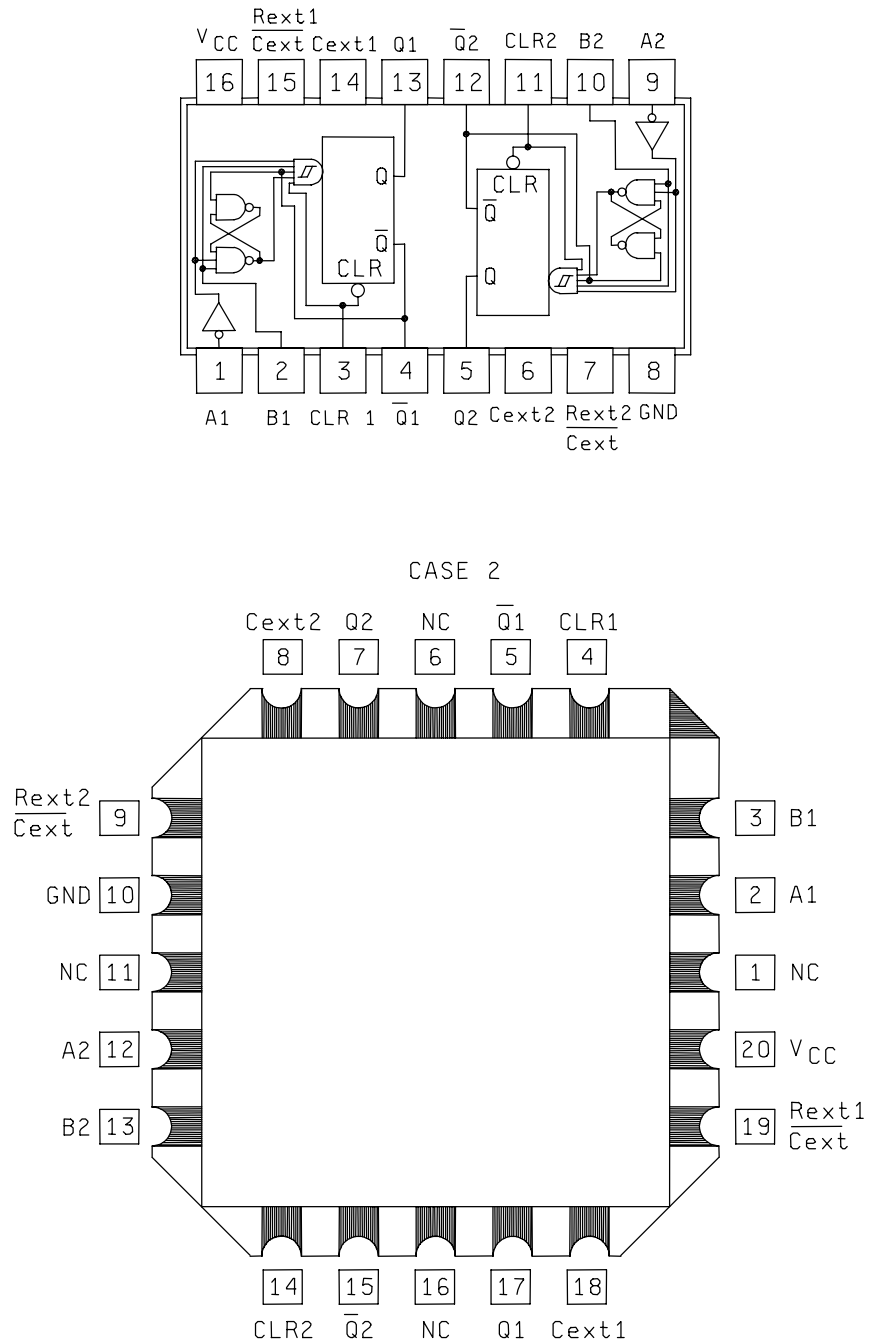
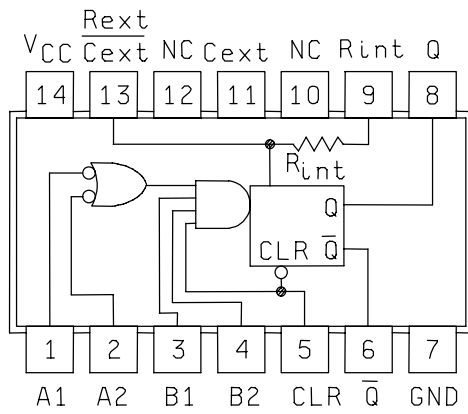


FIGURE 1. Terminal connections - Continued.

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DEVICE TYPE 03
CASES A,B,C,AND D



CASE 2

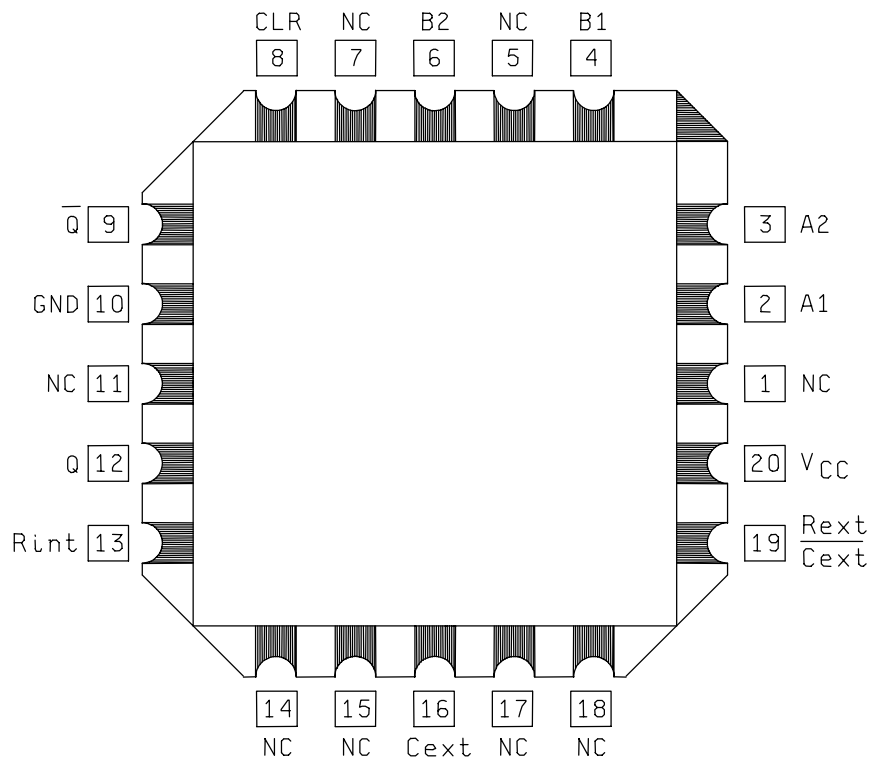


FIGURE 1. Terminal connections - Continued.

Device type 01 and 02

INPUTS			OUTPUTS	
CLEAR	A	B	Q	$\bar{Q}$
L	X	X	L	H
X	H	X	L	H
X	X	L	L	H
H	L	↑	□	⊐
H	↓	H	□	⊐
↑	L	H	□	⊐

Device type 03

INPUTS					OUTPUTS	
CLEAR	A1	A2	B1	B2	Q	$\bar{Q}$
L	X	X	X	X	L	H
X	H	H	X	X	L	H
X	X	X	L	X	L	H
X	X	X	X	L	L	H
H	L	X	↑	H	□	⊐
H	L	X	H	↑	□	⊐
H	X	L	↑	H	□	⊐
H	X	L	H	↑	□	⊐
H	H	↓	H	H	□	⊐
H	↓	↓	H	H	□	⊐
H	↓	H	H	H	□	⊐
↑	L	X	H	H	□	⊐
↑	X	L	H	H	□	⊐

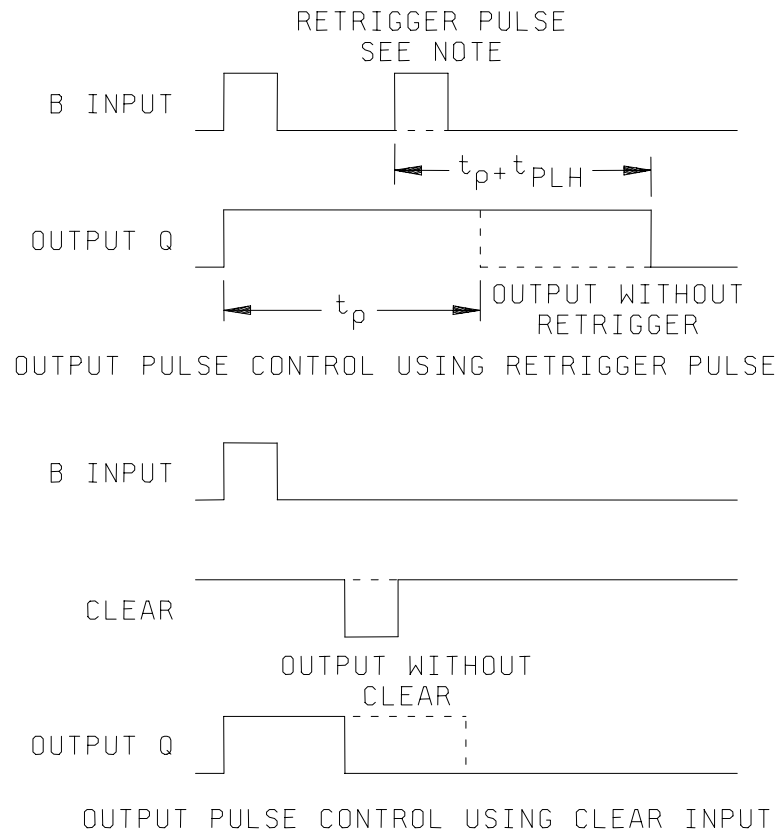
NOTES:

1. H = high level (steady state), L = low level (steady state), ↑ = transition from low to high level,
↓ = transition from high to low level, □ = one high level pulse, ⊐ = one low level pulse,
X = irrelevant (any input, including transitions).
2. To use the internal timing resistor of device type 03 connect Rint to V_{CC}.
3. An external timing capacitor may be connected between Cext and Rext/Cext (positive).
4. For accurate repeatable pulse widths, connect an external resistor between Rext/Cext and V_{CC} with Rint open circuited.
5. To obtain variable pulse widths, connect external variable resistance between Rint or Rext/Cext and V_{CC}.

FIGURE 2. Truth table and functional description.

Device types 01 and 03

This multivibrator features d-c triggering from gated low level active (A inputs), and high level active (B inputs). Output pulse width is a function of external capacitor and resistor values. Retriggering of input before output terminates, extends output pulse width. Overriding clear feature permits termination of output pulse width at a predetermined time independent of R and C timing components.

**NOTE:**

Retrigger pulse must not start before
 0.22 Cext (in picofarads) nanoseconds
 after previous trigger pulse.

FIGURE 2. Truth table and functional description - Continued.

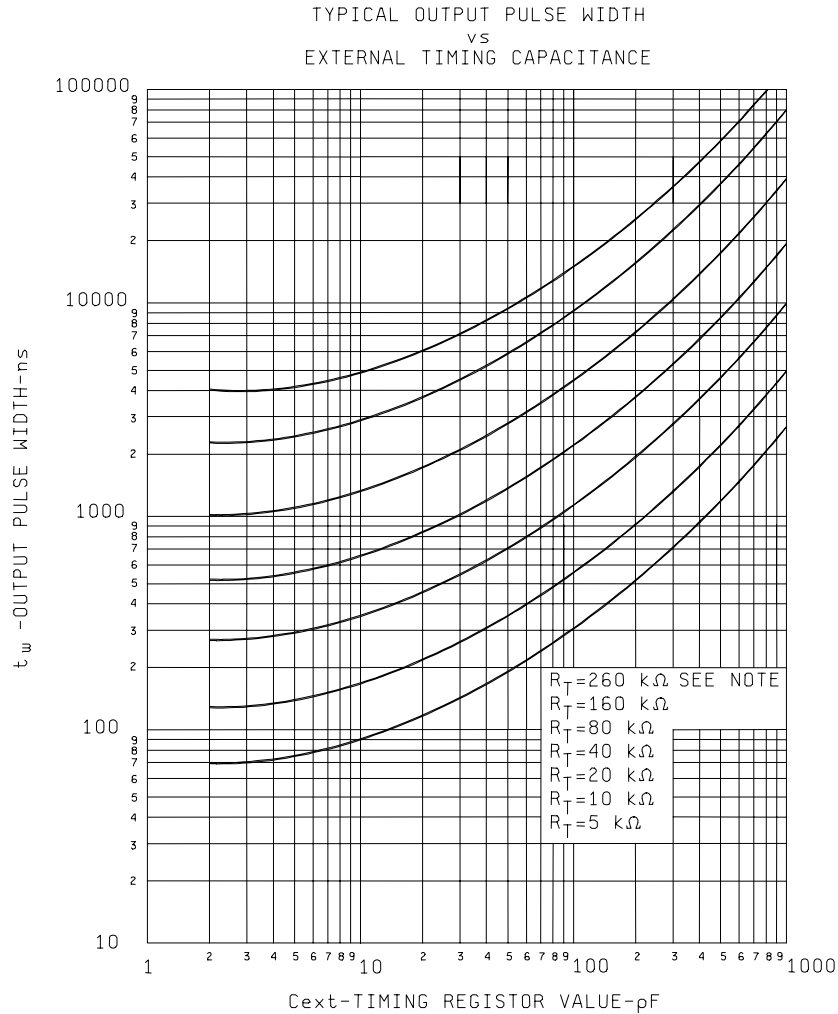
Device type 01 and 03 (Continued)

The output pulse width (t_p) is a function of the external capacitor and resistor values.

For output pulse widths when $C_{ext} > 1,000$ pF, t_p is defined as :

$$t_p \approx 0.4 R_T C_{ext} \quad \text{Where } R_T \text{ is in } k\Omega, C_{ext} \text{ is in pF, and } t_p \text{ is in ns.}$$

For output pulse widths when $C_{ext} < 1,000$ pF, t_p is defined as:

**NOTE:**

This value of resistance exceeds the maximum recommended for use over the full temperature range

FIGURE 2. Truth table and functional description - Continued.

Device type 02

This multivibrator features a negative transition triggered input and a positive transition triggered input, either of which can be used as an inhibit input. Pulse triggering occurs at a particular voltage level, not directly related to transition time of input pulse. Once fired, the outputs are independent of further transitions of A and B inputs, and are a function of the timing components. Output pulses can be terminated by the overriding clear, independent of R and C timing components.

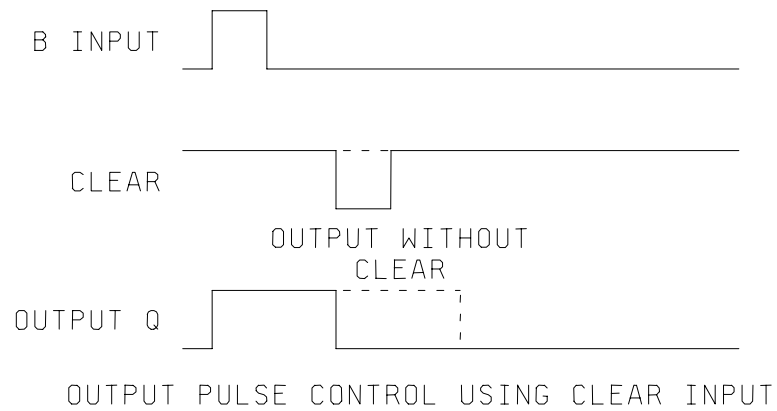


FIGURE 2. Truth table and functional description - Continued.

Device type 02 (Continued)

The output pulse width (t_p) is a function of the external capacitor and resistor values.

The output pulse width is defined as :

$$t_p \approx 0.7 R_T C_{ext} \quad \text{Where } R_T \text{ is in } k\Omega, C_{ext} \text{ is in pF, and } t_p \text{ is in ns.}$$

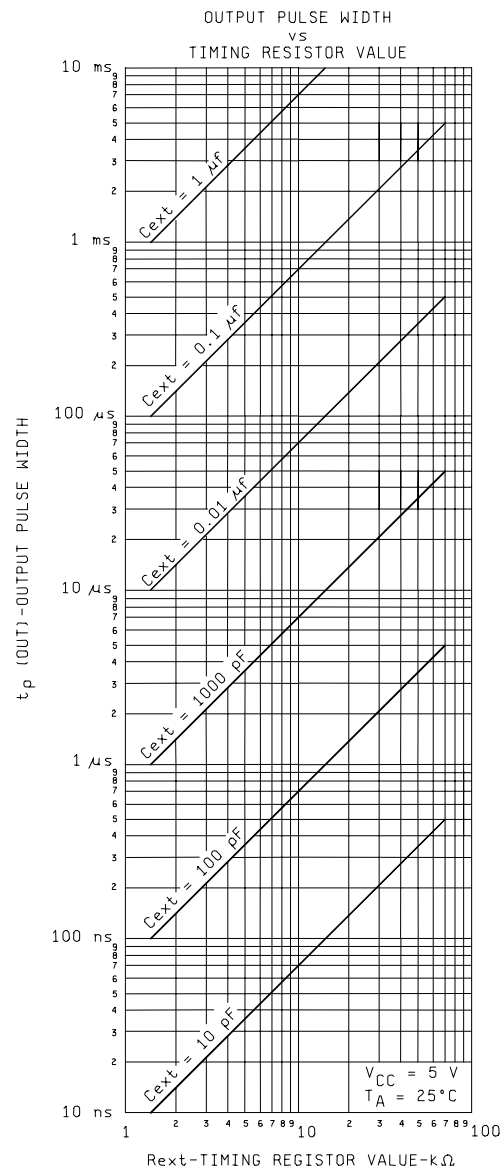
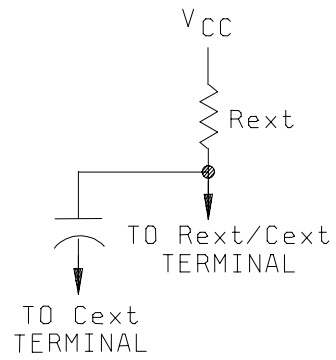


FIGURE 2. Truth table and functional description - Continued.

DEVICE TYPES 01,02 AND 03



TIMING COMPONENT CONNECTIONS

FIGURE 2. Truth table and functional description - Continued.

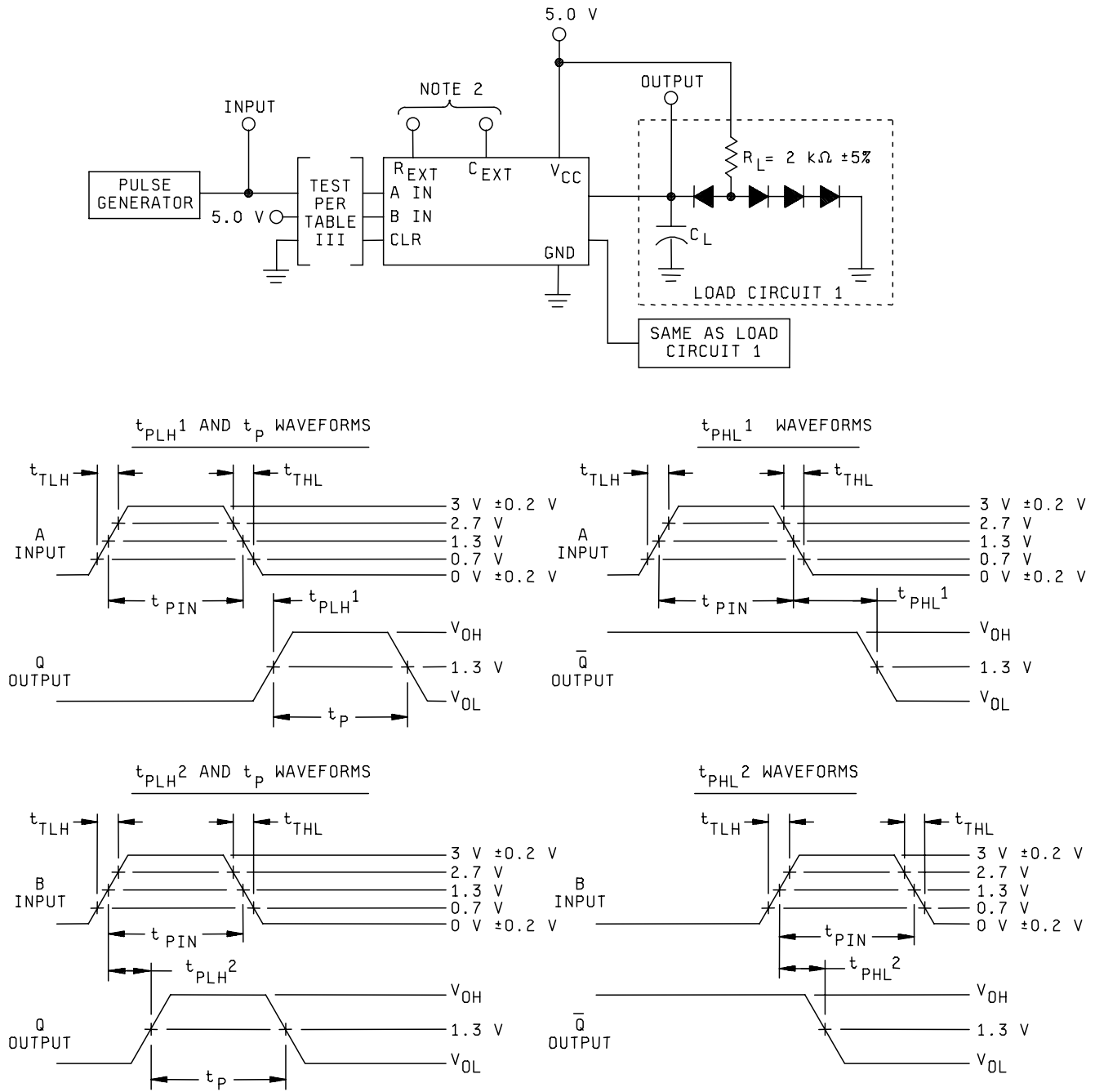
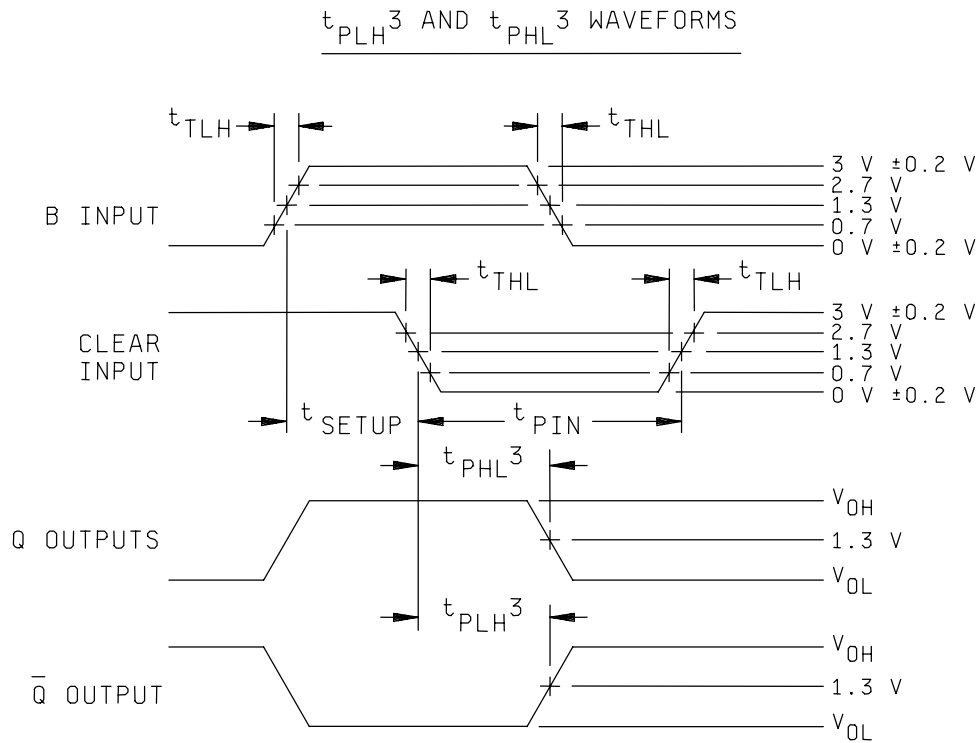


FIGURE 3. Switching test circuit and waveforms for device types 01 and 03.



NOTES:

1. Pulse generator has the following characteristics: $PRR \leq 1.0$ MHz, $t_{PIN} \geq 40$ ns, $t_{THL} \leq 6$ ns, $t_{TLH} \leq 15$ ns, and $Z_{OUT} = 50\Omega$.
2. See table III notes for R_{ext} , C_{ext} values.
3. $C_L = 50$ pF $\pm 10\%$ including scope probe, wiring and stray capacitance, without package in test fixture.
4. All diodes are 1N3064, or 1N916 or equivalent.
5. Load circuit on a given output is only required where the specified test in table III indicates "OUT" on that output.
6. t_{SETUP} (max) shall be $\leq 50\%$ of the typical output pulse width for the actual C_{ext} used (see figure 2).

FIGURE 3. Switching test circuit and waveforms for device types 01 and 03 - Continued.

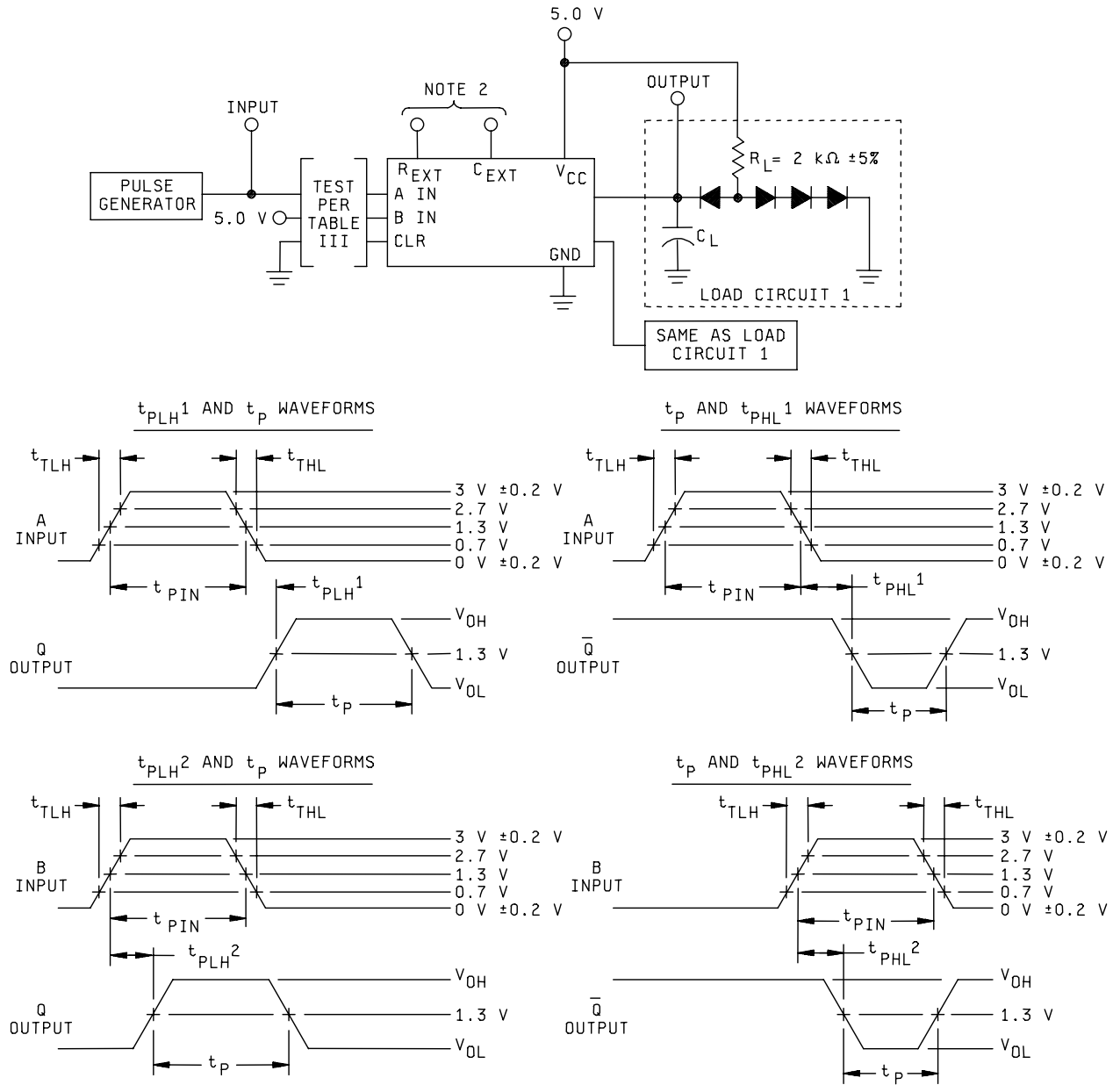
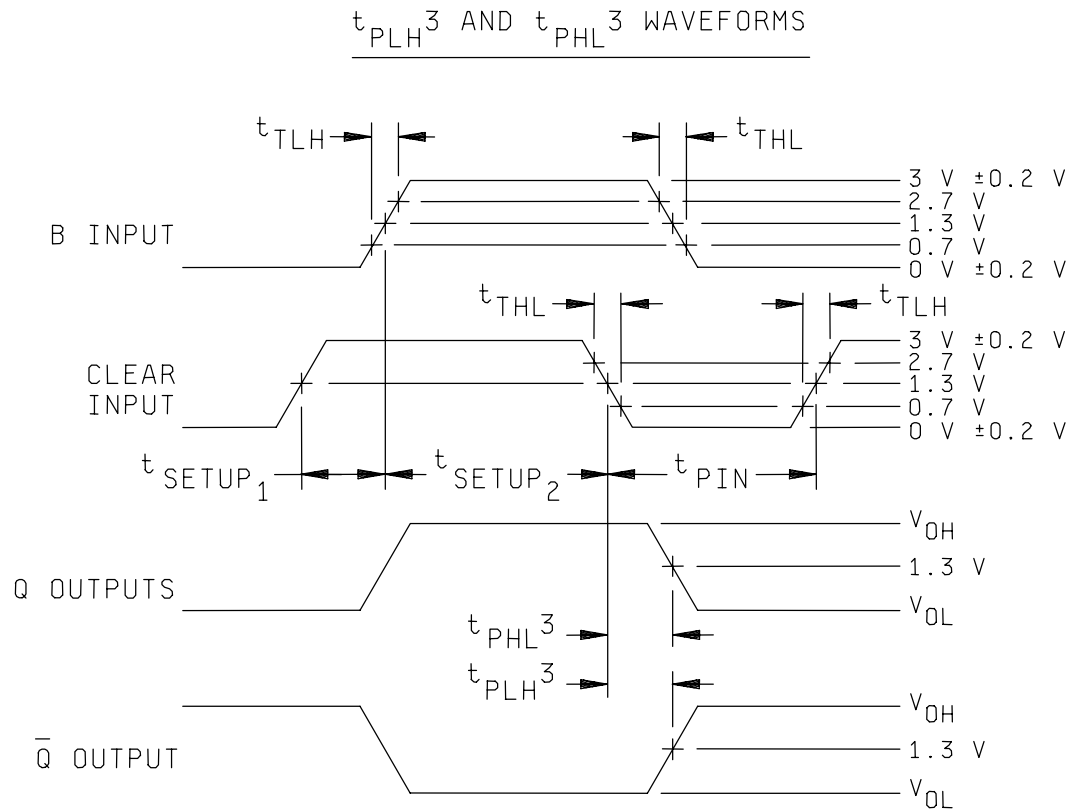


FIGURE 4. Switching test circuit and waveforms for device type 02.



NOTES:

1. Pulse generator has the following characteristics: $PRR \leq 1.0 \text{ MHz}$, $t_{PIN} \geq 40 \text{ ns}$, $t_{THL} \leq 6 \text{ ns}$, $t_{TLH} \leq 15 \text{ ns}$, t_{SETUP1} (CLEAR INACTIVE) = 15 ns and $Z_{OUT} = 50\Omega$.
2. See table III notes for Rext, Cext values.
3. $C_L = 50 \text{ pF} \pm 10\%$ including scope probe, wiring and stray capacitance, without package in test fixture.
4. All diodes are 1N3064, or 1N916 or equivalent.
5. Load circuit on a given output is only required where the specified test in table III indicates "OUT" on that output.
6. t_{SETUP} (max) shall be $\leq 50\%$ of the typical output pulse width for the actual Cext used (see figure 2).

FIGURE 4. Switching test circuit and waveforms for device type 02 - Continued.

TABLE III. Group A inspection for device type 01.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.7 V, or open).

Subgroup	Symbol	MIL-STD-883 method -	Cases E, F Case 2	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Limits	Unit
1	V_{OH} $T_C = 25^\circ\text{C}$	3006	Test no.	A1	B1	CLR1	Q1	Q2	Cext12	Rext12	GND	A2	B2	CLR2	Q2	Q1	Cext1	Rext1	V _{CC}		Min	Max
				2.0 V	0.7 V	2.0 V	-4 mA				GND								4.5 V	Q1	2.5	
				C	2.0 V	2.0 V										-4 mA		GND	"	Q1	"	
																			"	Q2	"	
								-4 mA		GND		C	2.0 V	2.0 V					"	Q2	"	
				5	2.0 V	0.7 V										4 mA			"	Q1		0.4
				C	2.0 V	2.0 V	4 mA											GND	"	Q1		
								4 mA											"	Q2		
				7								2.0 V	0.7 V	2.0 V					"	Q2		
										GND		C	2.0 V	2.0 V	4 mA				"	Q2		
	IC	3007		9	-18 mA														"	A1		-1.5
				10															"	B1		
				11		-18 mA													"	CLR1		
				12								-18 mA							"	A2		
				13									-18 mA						"	B2		
				14										-18 mA					"	CLR2		
				15	0.4 V														5.5 V	A1	-160	-400
				16		0.4 V													"	B1		
				17															"	CLR1		
				18								0.4 V							"	A2		
	IH1	3010	See note K	19									0.4 V						"	B2		
				20										0.4 V					"	CLR2		
				21	2.7 V														"	A1		20
				22		2.7 V													"	B1		
				23			2.7 V												"	CLR1		
				24								2.7 V							"	A2		
				25									2.7 V						"	B2		
				26										2.7 V					"	CLR2		
				27	5.5 V														"	A1		100
				28		5.5 V													"	B1		
	IH2	3011	OS	29			5.5 V												"	CLR1		
				30								5.5 V							"	A2		
				31									5.5 V						"	B2		
				32										5.5 V					"	CLR2		
				33 CKT A, D	GND	GND	GND												"	Q1	-15	-100
				33 CKT C	GND	GND	GND												"	Q1	-30	-130
				34 CKT A, D	C	4.5 V	4.5 V											GND	"	Q1	-15	-100
				34 CKT C	C	4.5 V	4.5 V											GND	"	Q1	-30	-130
				35 CKT A, D								GND	GND	GND	GND				"	Q2	-15	-100
				35 CKT C								GND	GND	GND	GND				"	Q2	-30	-130
		36 CKT A, D	C					GND		GND		C	4.5 V	4.5 V					"	Q2	-15	-100
								GND		GND		C	4.5 V	4.5 V					"	Q2	-30	-130

See footnotes at end of this table.

TABLE III. Group A inspection for device type 01.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.7 V, or open). 1/

Subgroup	Symbol	MIL-STD-883 method -	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Limits		Unit		
				2	3	4	5	7	8	9	10	12	13	14	15	16	Measured terminal	Min	Max					
Tc = 25°C	I _{CC2}	3005	37	5.5 V	5.5 V	5.5 V														V _{CC}		mA		
		3005	38	C	5.5 V	5.5 V														5.5 V		mA		
2	Same tests, terminal conditions, and limits as for subgroup 1, except T _C = +125°C and V _{CC} tests are omitted.																							
3	Same tests, terminal conditions, and limits as for subgroup 1, except T _C = -55°C and V _{CC} tests are omitted.																							
Tc = 25°C	Truth table tests	3014	39	A	A	B	H	L	F	F	GND	A	A	B	B	H	L	F	F	5.0 V	All outputs	See notes A, B, D, E and F		
			40	A	B	"	"	"	"	"	"	"	"	A	B	"	"	"	"	"	"		"	
			41	B	A	"	"	"	"	"	"	"	"	B	A	"	"	"	"	"	"		"	
			42	"	"	A	L	H	"	"	"	"	"	"	"	A	L	H	"	"	"		"	
			43	"	"	B	H	L	"	"	"	"	"	"	"	B	H	L	"	"	"		"	
			44	"	B	B	"	"	"	"	"	"	"	"	"	B	B	"	"	"	"		"	
			45	"	B	A	"	"	"	"	"	"	"	"	"	A	"	"	"	"	"		"	
			46	"	A	A	L	H	"	"	"	"	"	"	"	A	L	H	"	"	"		"	
			47	"	"	B	H	L	"	"	"	"	"	"	"	B	H	L	"	"	"		"	
			48	A	"	B	"	"	"	"	"	"	"	A	"	"	B	"	"	"	"		"	
			49	A	"	A	"	"	"	"	"	"	"	A	"	"	A	"	"	"	"		"	
			50	B	"	A	L	H	"	"	"	"	"	B	"	"	A	L	H	"	"		"	
			51	B	B	B	H	L	"	"	"	"	"	B	B	B	H	L	"	"	"		"	
			52	A	B	A	"	"	"	"	"	"	"	A	B	A	"	"	"	"	"		"	
			53	"	A	"	"	"	"	"	"	"	"	"	"	A	"	"	"	"	"		"	
			54	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	
			55	B	B	"	"	"	"	"	"	"	"	B	B	B	"	"	"	"	"		"	
8	Repeat subgroup 7 at T _C = +125°C and T _C = -55°C.																							
Tc = 25°C	t _{PLH1}	3003 Fig. 3	56	IN	5.0 V	5.0 V					GND			5.0 V		OUT	F	F	5.0 V	A1 to Q1	5	38	ns	
			57												5.0 V						"	A2 to Q2	"	38
	t _{PLH2}	"	58											5.0 V						"	B2 to Q2	"	49	
			59	GND	IN	5.0 V										IN		OUT	F	F	"	B1 to Q1	"	49
	t _{PLH3}	"	60	GND	IN	IN	OUT											L	L	"	CLR1 to Q1	"	50	
			61														OUT			"	CLR2 to Q2	"	"	
	t _{PHL1}	"	62											5.0 V	5.0 V	OUT				"	A2 to Q2	"	"	
			63	IN	5.0 V	5.0 V	OUT											F	F	"	A1 to Q1	"	"	
	t _{PHL2}	"	64	GND	IN	5.0 V	OUT												F	F	"	B1 to Q1	"	61
			65												5.0 V	5.0 V	OUT			"	B2 to Q2	"	61	
t _{PHL3}	"	66						OUT	L	L	"	GND	IN	IN						CLR2 to Q2	"	32		
		67	GND	IN	IN												OUT	L	L	"	CLR1 to Q1	"	32	
t _{P(MIN)}	"	68	IN	5.0 V	5.0 V											OUT	Open	G	G	"	Q1	205	"	
		69	GND	IN	5.0 V					OPEN	G	"					OUT	Open	G	"	Q1	"	"	
		70					OUT	OPEN	G	G	"	IN	5.0 V	5.0 V						"	Q2	"	"	
																			"	Q2	"	"		

TABLE III. Group A inspection for device type 01.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.7 V, or open). 1/

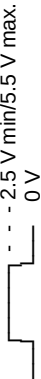
			Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Limits		Unit																				
Subgroup	Symbol	MIL-STD-883 method -	Case 2	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20		Min	Max																					
9 T _C = 25°C				A1	B1	CLR1	$\bar{Q}1$	Q2	Cex12	Rex12	GND	A2	B2	CLR2	$\bar{Q}2$	Q1	Cex11	Rex11	V _{CC}																								
		3003	72					OUT	I	I	GND	GND	IN	5.0 V	5.0 V				5.0 V	Q2	3.5	6.0	μs																				
		"	73					OUT	I	I	"	IN	5.0 V	5.0 V	5.0 V				"	Q2	"	"	"																				
		"	74	IN	5.0 V	5.0 V											OUT	I	I	"	Q1	"	"	"																			
10	Same tests and terminal conditions as subgroup 9, except T _C = +125°C and limits are as follows: t _{PLH1} is 5 to 57 ns; t _{PLH2} is 5 to 74 ns; t _{PLH3} is 5 to 75 ns; t _{PLH1} is 5 to 75 ns; t _{PLH2} is 5 to 92 ns; t _{PLH3} is 5 to 48 ns; t _{PLH3} is 308 ns; and t _{PLH3} is 3.0 to 6.25 μs.																																										
11	Same tests and terminal conditions as for subgroup 10, except T _C = -55°C.																																										

NOTES:

A. V_{IN} = 3.0 V minimum.

B. V_{IN} = 0.0 V or GND.

C. Apply input pulse



D. Test numbers 39 through 55 shall be run in sequence.

E. Output voltages shall be either:

H > 1.5 V; L < 1.5 V

F. Rext = 5 k Ω minimum to 180 k Ω maximum, connected to V_{CC}; Cext $\leq$ 1,000 μ F, connected to Rext terminal.

G. Rext = 5 k Ω $\pm$ 10%, connected to V_{CC}.

I. Rext = 10 k Ω $\pm$ 10%, connect to V_{CC}; Cext $\leq$ 1,000 μ F $\pm$ 10%, connected to Rext terminal.

J. During subgroups 9, 10, 11 testing, Rext and Cext may remain applied on the side of the device not under test if desired.

K. For circuit D, I_{IL1} limits are 120 mA to 360 mA.

L. Rext = 10 k Ω $\pm$ 10%, connect to V_{CC}; Cext $\geq$ 45 pF connected to Rext terminal.

TABLE III. Group A inspection for device type 02.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.7 V, or open).

Subgroup	Symbol	MIL-STD-883 method -	Cases E, F, Case 2	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Limits	Unit
			Test no.	A1	B1	CLR1	Q1	Q2	Cex12 See L	Rext2 See L	GND	A2	B2	CLR2	Q2	Q1	Cex1 See L	Rext1 See L	V _{CC}		Min	Max
1	V _{OH}	3006	1	2.0 V	0.7 V	2.0 V	-4 mA				GND						GND		4.5 V	Q1	2.5	
		"	2	C	2.0 V	2.0 V					"	2.0 V	0.7 V	2.0 V	-4 mA	-4 mA	GND		"	Q1	"	"
		"	3								"	"	C	2.0 V					"	Q2	"	"
		"	4					-4 mA	GND		"	"	"	2.0 V		4 mA	GND		"	Q2	"	0.4
		3007	5	2.0 V	0.7 V	2.0 V	4 mA				"	"	"	2.0 V					"	Q1	"	"
		"	6	C	2.0 V	2.0 V					"	"	"	2.0 V					"	Q2	"	"
		"	7					4 mA	GND		"	"	"	2.0 V	4 mA				"	Q1	"	"
		"	8								"	"	"	2.0 V					"	Q2	"	"
	I _C		9	-18 mA							"	"	"						"	A1	-1.5	"
			10		-18 mA						"	"	"						"	B1	"	"
			11								"	"	"						"	CLR1	"	"
			12								"	"	-18 mA						"	A2	"	"
			13								"	"		-18 mA					"	B2	"	"
			14								"	"							"	CLR2	"	"
I _{L2}		3009	15 CKT A, C, D	0.4 V	4.5 V	4.5 V					"	"							5.5 V	A1	-100	-340
		"	15 CKT B	"	"	"					"	"							"	A1	-30	-300
		"	15 CKT E	"	"	"					"	"							"	A1	-160	-400
		"	16 CKT A, C	GND	0.4 V	"					"	"							"	B1	-160	-580
		"	16 CKT B	"	"	"					"	"							"	B1	-30	-300
		"	16 CKT E, D	"	"	"					"	"							"	B1	-200	-440
		"	17 CKT A, C, D, E	"	4.5 V	GND					"	"					GND		"	CLR1	-200	-680
		"	17 CKT B	"	4.5 V	GND					"	"	0.4 V	4.5 V	4.5 V		GND		"	CLR1	-30	-300
		"	18 CKT A, C, D								"	"	"	"	"				"	A2	-100	-340
		"	18 CKT B								"	"	"	"	"				"	A2	-30	-300
		"	18 CKT E								"	"	"	"	"				"	A2	-160	-400
		"	19 CKT A, C								"	"	GND	0.4 V	"				"	B2	-160	-580
		"	19 CKT B								"	"	"	"	"				"	B2	-30	-300
		"	19 CKT E, D								"	"	"	"	"				"	B2	-200	-440
I _{H1}			20 CKT A, C, D, E						GND	GND	"	"	4.5 V	GND					"	CLR2	-200	-680
			20 CKT B						GND	GND	"	"	4.5 V	GND					"	CLR2	-30	-300
		3010	21	2.7 V							"	"							"	A1	20	"
		"	22	4.5 V	2.7 V	GND					"	"							"	B1	"	"
		"	23	4.5 V	GND	2.7 V					"	"							"	CLR1	"	"
		"	24								"	"	2.7 V						"	A2	"	"
		"	25								"	"	4.5 V	GND					"	B2	"	"
		"	26								"	"	4.5 V	2.7 V					"	CLR2	"	"
		"	27	5.5 V							"	"							"	A1	100	"
		"	28	4.5 V	5.5 V	GND					"	"							"	B1	"	"
		"	29	4.5 V	GND	5.5 V					"	"							"	CLR1	"	"
		"	30								"	"	5.5 V						"	A2	"	"
		"	31								"	"	4.5 V	5.5 V	GND				"	B2	"	"
		"	32								"	"	4.5 V	5.5 V	5.5 V				"	CLR2	"	"

See footnotes at end of this table.

TABLE III. Group A inspection for device type 02 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.7 V, or open).

Subgroup	Symbol	MIL-STD-883 method -	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Limits	Unit						
1	I _{OS}	3011	Case 2	A1	B1	CLR1	Q1	Q2	Cex1 See L	Rex2 See L	GND	A2	B2	CLR2	Q2	Q1	Cex1 See L	Rex1 See L	V _{CC}	Q1	-15	-100	mA					
				C	4.5 V	4.5 V																						
				C	4.5 V	4.5 V																Q1	-30	-130	"			
				34 CKT																		Q2	-15	-100	"			
				A, B, D, E																		Q2	-30	-130	"			
				34 CKT C																		Q2	-15	-100	"			
				35 CKT																		Q2	-15	-100	"			
				A, B, D, E																		Q2	-30	-130	"			
				35 CKT C																		Q2	-30	-130	"			
				36 CKT																		Q2	-15	-100	"			
2	3005	3005	37	5.5 V	GND	5.5 V						5.5 V	GND	5.5 V						V _{CC}	11	"						
				C	5.5 V	5.5 V						C	5.5 V	5.5 V							V _{CC}	27	"					
				Same tests, terminal conditions, and limits as for subgroup 1, except T _C = +125°C and V _C tests are omitted.																								
				Same tests, terminal conditions, and limits as for subgroup 1, except T _C = -55°C and V _C tests are omitted.																								
				7	Truth table tests	3014	39	A	A	B	H	L	F	F	GND	A	A	A	B	H	L	F	F	5.0 V	All outputs	See notes A, B, D, E, and F		
							40	A	B	"	"	"	"	"	"	"	"	A	B	"	"	"	"	"	"		"	"
							41	B	A	"	"	"	"	"	"	"	"	B	A	"	"	"	"	"	"		"	"
							42	"	"	A	L	H	"	"	"	"	"	"	"	A	L	H	"	"	"		"	"
							43	"	"	B	H	L	"	"	"	"	"	"	"	B	H	L	"	"	"		"	"
							44	"	B	B	"	"	"	"	"	"	"	"	B	B	"	"	"	"	"		"	"
45	"	B	A				"	"	"	"	"	"	"	"	B	A	"	"	"	"	"	"	"					
46	"	A	A				L	H	"	"	"	"	"	"	A	A	L	H	"	"	"	"	"					
47	"	"	B				H	L	"	"	"	"	"	"	"	B	H	L	"	"	"	"	"					
48	A	"	B				"	"	"	"	"	"	"	A	"	"	"	"	"	"	"	"	"					
8	Repeat subgroup 7 at T _C = +125°C and T _C = -55°C	3014	49	A	A	A	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	See notes A, B, D, E, and F						
			50	B	"	A	L	"	"	"	"	"	"	"	"	"	"	H	"	"	"		"	"				
			51	"	"	B	H	"	"	"	"	"	"	"	"	"	"	L	"	"	"		"	"				
			52	"	"	"	"	"	"	"	"	"	"	"	"	A	"	"	"	"	"		"	"				
			53	"	"	"	"	"	"	"	"	"	"	B	"	A	L	"	"	"	"		"	"				
			54	"	B	"	"	"	"	"	"	"	"	"	B	B	H	"	"	"	"		"	"				
			55	A	B	A	"	"	"	"	"	"	"	A	B	A	"	"	"	"	"		"	"				
			56	"	A	"	"	"	"	"	"	"	"	"	A	"	"	"	"	"	"		"	"				
			57	"	B	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"				
			58	B	B	"	"	"	"	"	"	"	"	B	B	"	"	"	"	"	"		"	"				

See footnotes at end of this table.

See notes at end of this table.

[illegible]

TABLE III. Group A inspection for device type 02 - Continued.

			1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16		Unit			
Subgroup	Symbol	MIL-STD-883 method -	Cases E, F	2	3	4	5	7	8	9	10	12	13	14	15	16	17	18	19	20			
	Test no.	A1	B1	CLR1	Q1	Q2	Cex2	Rex12	GND	A2	B2	CLR2	Q2	Q1	Cex11	Rex11	V _{cc}						
9	t _{ps}	3003	98				OUT	J	J	GND	IN	5.0 V	5.0 V										
			99	IN	5.0 V	5.0 V																	
			100	IN	5.0 V		OUT																
			101	GND	IN		OUT																
			102	GND	IN																		
10	Same tests and terminal conditions as subgroup 9, except T _C = +125°C and limits are as follows: t _{PHL1} is 5 to 113 ns; t _{PHL2} is 5 to 105 ns; t _{PHL3} is 5 to 128 ns; t _{PHL4} is 5 to 105 ns; t _{PHL5} is 5 to 105 ns; t _{PHL6} is 5 to 90 ns; t _{PHL7} is 5 to 90 ns; t _{PHL8} is 5 to 90 ns; t _{PHL9} is 5 to 90 ns; t _{PHL10} is 5 to 90 ns; t _{PHL11} is 5 to 90 ns; t _{PHL12} is 5 to 90 ns; t _{PHL13} is 5 to 90 ns; t _{PHL14} is 5 to 90 ns; t _{PHL15} is 5 to 90 ns; t _{PHL16} is 5 to 90 ns; t _{PHL17} is 5 to 90 ns; t _{PHL18} is 5 to 90 ns; t _{PHL19} is 5 to 90 ns; t _{PHL20} is 5 to 90 ns; t _{PHL21} is 5 to 90 ns; t _{PHL22} is 5 to 90 ns; t _{PHL23} is 5 to 90 ns; t _{PHL24} is 5 to 90 ns; t _{PHL25} is 5 to 90 ns; t _{PHL26} is 5 to 90 ns; t _{PHL27} is 5 to 90 ns; t _{PHL28} is 5 to 90 ns; t _{PHL29} is 5 to 90 ns; t _{PHL30} is 5 to 90 ns; t _{PHL31} is 5 to 90 ns; t _{PHL32} is 5 to 90 ns; t _{PHL33} is 5 to 90 ns; t _{PHL34} is 5 to 90 ns; t _{PHL35} is 5 to 90 ns; t _{PHL36} is 5 to 90 ns; t _{PHL37} is 5 to 90 ns; t _{PHL38} is 5 to 90 ns; t _{PHL39} is 5 to 90 ns; t _{PHL40} is 5 to 90 ns; t _{PHL41} is 5 to 90 ns; t _{PHL42} is 5 to 90 ns; t _{PHL43} is 5 to 90 ns; t _{PHL44} is 5 to 90 ns; t _{PHL45} is 5 to 90 ns; t _{PHL46} is 5 to 90 ns; t _{PHL47} is 5 to 90 ns; t _{PHL48} is 5 to 90 ns; t _{PHL49} is 5 to 90 ns; t _{PHL50} is 5 to 90 ns; t _{PHL51} is 5 to 90 ns; t _{PHL52} is 5 to 90 ns; t _{PHL53} is 5 to 90 ns; t _{PHL54} is 5 to 90 ns; t _{PHL55} is 5 to 90 ns; t _{PHL56} is 5 to 90 ns; t _{PHL57} is 5 to 90 ns; t _{PHL58} is 5 to 90 ns; t _{PHL59} is 5 to 90 ns; t _{PHL60} is 5 to 90 ns; t _{PHL61} is 5 to 90 ns; t _{PHL62} is 5 to 90 ns; t _{PHL63} is 5 to 90 ns; t _{PHL64} is 5 to 90 ns; t _{PHL65} is 5 to 90 ns; t _{PHL66} is 5 to 90 ns; t _{PHL67} is 5 to 90 ns; t _{PHL68} is 5 to 90 ns; t _{PHL69} is 5 to 90 ns; t _{PHL70} is 5 to 90 ns; t _{PHL71} is 5 to 90 ns; t _{PHL72} is 5 to 90 ns; t _{PHL73} is 5 to 90 ns; t _{PHL74} is 5 to 90 ns; t _{PHL75} is 5 to 90 ns; t _{PHL76} is 5 to 90 ns; t _{PHL77} is 5 to 90 ns; t _{PHL78} is 5 to 90 ns; t _{PHL79} is 5 to 90 ns; t _{PHL80} is 5 to 90 ns; t _{PHL81} is 5 to 90 ns; t _{PHL82} is 5 to 90 ns; t _{PHL83} is 5 to 90 ns; t _{PHL84} is 5 to 90 ns; t _{PHL85} is 5 to 90 ns; t _{PHL86} is 5 to 90 ns; t _{PHL87} is 5 to 90 ns; t _{PHL88} is 5 to 90 ns; t _{PHL89} is 5 to 90 ns; t _{PHL90} is 5 to 90 ns; t _{PHL91} is 5 to 90 ns; t _{PHL92} is 5 to 90 ns; t _{PHL93} is 5 to 90 ns; t _{PHL94} is 5 to 90 ns; t _{PHL95} is 5 to 90 ns; t _{PHL96} is 5 to 90 ns; t _{PHL97} is 5 to 90 ns; t _{PHL98} is 5 to 90 ns; t _{PHL99} is 5 to 90 ns; t _{PHL100} is 5 to 90 ns; t _{PHL101} is 5 to 90 ns; t _{PHL102} is 5 to 90 ns; t _{PHL103} is 5 to 90 ns; t _{PHL104} is 5 to 90 ns; t _{PHL105} is 5 to 90 ns; t _{PHL106} is 5 to 90 ns; t _{PHL107} is 5 to 90 ns; t _{PHL108} is 5 to 90 ns; t _{PHL109} is 5 to 90 ns; t _{PHL110} is 5 to 90 ns; t _{PHL111} is 5 to 90 ns; t _{PHL112} is 5 to 90 ns; t _{PHL113} is 5 to 90 ns; t _{PHL114} is 5 to 90 ns; t _{PHL115} is 5 to 90 ns; t _{PHL116} is 5 to 90 ns; t _{PHL117} is 5 to 90 ns; t _{PHL118} is 5 to 90 ns; t _{PHL119} is 5 to 90 ns; t _{PHL120} is 5 to 90 ns; t _{PHL121} is 5 to 90 ns; t _{PHL122} is 5 to 90 ns; t _{PHL123} is 5 to 90 ns; t _{PHL124} is 5 to 90 ns; t _{PHL125} is 5 to 90 ns; t _{PHL126} is 5 to 90 ns; t _{PHL127} is 5 to 90 ns; t _{PHL128} is 5 to 90 ns; t _{PHL129} is 5 to 90 ns; t _{PHL130} is 5 to 90 ns; t _{PHL131} is 5 to 90 ns; t _{PHL132} is 5 to 90 ns; t _{PHL133} is 5 to 90 ns; t _{PHL134} is 5 to 90 ns; t _{PHL135} is 5 to 90 ns; t _{PHL136} is 5 to 90 ns; t _{PHL137} is 5 to 90 ns; t _{PHL138} is 5 to 90 ns; t _{PHL139} is 5 to 90 ns; t _{PHL140} is 5 to 90 ns; t _{PHL141} is 5 to 90 ns; t _{PHL142} is 5 to 90 ns; t _{PHL143} is 5 to 90 ns; t _{PHL144} is 5 to 90 ns; t _{PHL145} is 5 to 90 ns; t _{PHL146} is 5 to 90 ns; t _{PHL147} is 5 to 90 ns; t _{PHL148} is 5 to 90 ns; t _{PHL149} is 5 to 90 ns; t _{PHL150} is 5 to 90 ns; t _{PHL151} is 5 to 90 ns; t _{PHL152} is 5 to 90 ns; t _{PHL153} is 5 to 90 ns; t _{PHL154} is 5 to 90 ns; t _{PHL155} is 5 to 90 ns; t _{PHL156} is 5 to 90 ns; t _{PHL157} is 5 to 90 ns; t _{PHL158} is 5 to 90 ns; t _{PHL159} is 5 to 90 ns; t _{PHL160} is 5 to 90 ns; t _{PHL161} is 5 to 90 ns; t _{PHL162} is 5 to 90 ns; t _{PHL163} is 5 to 90 ns; t _{PHL164} is 5 to 90 ns; t _{PHL165} is 5 to 90 ns; t _{PHL166} is 5 to 90 ns; t _{PHL167} is 5 to 90 ns; t _{PHL168} is 5 to 90 ns; t _{PHL169} is 5 to 90 ns; t _{PHL170} is 5 to 90 ns; t _{PHL171} is 5 to 90 ns; t _{PHL172} is 5 to 90 ns; t _{PHL173} is 5 to 90 ns; t _{PHL174} is 5 to 90 ns; t _{PHL175} is 5 to 90 ns; t _{PHL176} is 5 to 90 ns; t _{PHL177} is 5 to 90 ns; t _{PHL178} is 5 to 90 ns; t _{PHL179} is 5 to 90 ns; t _{PHL180} is 5 to 90 ns; t _{PHL181} is 5 to 90 ns; t _{PHL182} is 5 to 90 ns; t _{PHL183} is 5 to 90 ns; t _{PHL184} is 5 to 90 ns; t _{PHL185} is 5 to 90 ns; t _{PHL186} is 5 to 90 ns; t _{PHL187} is 5 to 90 ns; t _{PHL188} is 5 to 90 ns; t _{PHL189} is 5 to 90 ns; t _{PHL190} is 5 to 90 ns; t _{PHL191} is 5 to 90 ns; t _{PHL192} is 5 to 90 ns; t _{PHL193} is 5 to 90 ns; t _{PHL194} is 5 to 90 ns; t _{PHL195} is 5 to 90 ns; t _{PHL196} is 5 to 90 ns; t _{PHL197} is 5 to 90 ns; t _{PHL198} is 5 to 90 ns; t _{PHL199} is 5 to 90 ns; t _{PHL200} is 5 to 90 ns; t _{PHL201} is 5 to 90 ns; t _{PHL202} is 5 to 90 ns; t _{PHL203} is 5 to 90 ns; t _{PHL204} is 5 to 90 ns; t _{PHL205} is 5 to 90 ns; t _{PHL206} is 5 to 90 ns; t _{PHL207} is 5 to 90 ns; t _{PHL208} is 5 to 90 ns; t _{PHL209} is 5 to 90 ns; t _{PHL210} is 5 to 90 ns; t _{PHL211} is 5 to 90 ns; t _{PHL212} is 5 to 90 ns; t _{PHL213} is 5 to 90 ns; t _{PHL214} is 5 to 90 ns; t _{PHL215} is 5 to 90 ns; t _{PHL216} is 5 to 90 ns; t _{PHL217} is 5 to 90 ns; t _{PHL218} is 5 to 90 ns; t _{PHL219} is 5 to 90 ns; t _{PHL220} is 5 to 90 ns; t _{PHL221} is 5 to 90 ns; t _{PHL222} is 5 to 90 ns; t _{PHL223} is 5 to 90 ns; t _{PHL224} is 5 to 90 ns; t _{PHL225} is 5 to 90 ns; t _{PHL226} is 5 to 90 ns; t _{PHL227} is 5 to 90 ns; t _{PHL228} is 5 to 90 ns; t _{PHL229} is 5 to 90 ns; t _{PHL230} is 5 to 90 ns; t _{PHL231} is 5 to 90 ns; t _{PHL232} is 5 to 90 ns; t _{PHL233} is 5 to 90 ns; t _{PHL234} is 5 to 90 ns; t _{PHL235} is 5 to 90 ns; t _{PHL236} is 5 to 90 ns; t _{PHL237} is 5 to 90 ns; t _{PHL238} is 5 to 90 ns; t _{PHL239} is 5 to 90 ns; t _{PHL240} is 5 to 90 ns; t _{PHL241} is 5 to 90 ns; t _{PHL242} is 5 to 90 ns; t _{PHL243} is 5 to 90 ns; t _{PHL244} is 5 to 90 ns; t _{PHL245} is 5 to 90 ns; t _{PHL246} is 5 to 90 ns; t _{PHL247} is 5 to 90 ns; t _{PHL248} is 5 to 90 ns; t _{PHL249} is 5 to 90 ns; t _{PHL250} is 5 to 90 ns; t _{PHL251} is 5 to 90 ns; t _{PHL252} is 5 to 90 ns; t _{PHL253} is 5 to 90 ns; t _{PHL254} is 5 to 90 ns; t _{PHL255} is 5 to 90 ns; t _{PHL256} is 5 to 90 ns; t _{PHL257} is 5 to 90 ns; t _{PHL258} is 5 to 90 ns; t _{PHL259} is 5 to 90 ns; t _{PHL260} is 5 to 90 ns; t _{PHL261} is 5 to 90 ns; t _{PHL262} is 5 to 90 ns; t _{PHL263} is 5 to 90 ns; t _{PHL264} is 5 to 90 ns; t _{PHL265} is 5 to 90 ns; t _{PHL266} is 5 to 90 ns; t _{PHL267} is 5 to 90 ns; t _{PHL268} is 5 to 90 ns; t _{PHL269} is 5 to 90 ns; t _{PHL270} is 5 to 90 ns; t _{PHL271} is 5 to 90 ns; t _{PHL272} is 5 to 90 ns; t _{PHL273} is 5 to 90 ns; t _{PHL274} is 5 to 90 ns; t _{PHL275} is 5 to 90 ns; t _{PHL276} is 5 to 90 ns; t _{PHL277} is 5 to 90 ns; t _{PHL278} is 5 to 90 ns; t _{PHL279} is 5 to 90 ns; t _{PHL280} is 5 to 90 ns; t _{PHL281} is 5 to 90 ns; t _{PHL282} is 5 to 90 ns; t _{PHL283} is 5 to 90 ns; t _{PHL284} is 5 to 90 ns; t _{PHL285} is 5 to 90 ns; t _{PHL286} is 5 to 90 ns; t _{PHL287} is 5 to 90 ns; t _{PHL288} is 5 to 90 ns; t _{PHL289} is 5 to 90 ns; t _{PHL290} is 5 to 90 ns; t _{PHL291} is 5 to 90 ns; t _{PHL292} is 5 to 90 ns; t _{PHL293} is 5 to 90 ns; t _{PHL294} is 5 to 90 ns; t _{PHL295} is 5 to 90 ns; t _{PHL296} is 5 to 90 ns; t _{PHL297} is 5 to 90 ns; t _{PHL298} is 5 to 90 ns; t _{PHL299} is 5 to 90 ns; t _{PHL300} is 5 to 90 ns; t _{PHL301} is 5 to 90 ns; t _{PHL302} is 5 to 90 ns; t _{PHL303} is 5 to 90 ns; t _{PHL304} is 5 to 90 ns; t _{PHL305} is 5 to 90 ns; t _{PHL306} is 5 to 90 ns; t _{PHL307} is 5 to 90 ns; t _{PHL308} is 5 to 90 ns; t _{PHL309} is 5 to 90 ns; t _{PHL310} is 5 to 90 ns; t _{PHL311} is 5 to 90 ns; t _{PHL312} is 5 to 90 ns; t _{PHL313} is 5 to 90 ns; t _{PHL314} is 5 to 90 ns; t _{PHL315} is 5 to 90 ns; t _{PHL316} is 5 to 90 ns; t _{PHL317} is 5 to 90 ns; t _{PHL318} is 5 to 90 ns; t _{PHL319} is 5 to 90 ns; t _{PHL320} is 5 to 90 ns; t _{PHL321} is 5 to 90 ns; t _{PHL322} is 5 to 90 ns; t _{PHL323} is 5 to 90 ns; t _{PHL324} is 5 to 90 ns; t _{PHL325} is 5 to 90 ns; t _{PHL326} is 5 to 90 ns; t _{PHL327} is 5 to 90 ns; t _{PHL328} is 5 to 90 ns; t _{PHL329} is 5 to 90 ns; t _{PHL330} is 5 to 90 ns; t _{PHL331} is 5 to 90 ns; t _{PHL332} is 5 to 90 ns; t _{PHL333} is 5 to 90 ns; t _{PHL334} is 5 to 90 ns; t _{PHL335} is 5 to 90 ns; t _{PHL336} is 5 to 90 ns; t _{PHL337} is 5 to 90 ns; t _{PHL338} is 5 to 90 ns; t _{PHL339} is 5 to 90 ns; t _{PHL340} is 5 to 90 ns; t _{PHL341} is 5 to 90 ns; t _{PHL342} is 5 to 90 ns; t _{PHL343} is 5 to 90 ns; t _{PHL344} is 5 to 90 ns; t _{PHL345} is 5 to 90 ns; t _{PHL346} is 5 to 90 ns; t _{PHL347} is 5 to 90 ns; t _{PHL348} is 5 to 90 ns; t _{PHL349} is 5 to 90 ns; t _{PHL350} is 5 to 90 ns; t _{PHL351} is 5 to 90 ns; t _{PHL352} is 5 to 90 ns; t _{PHL353} is 5 to 90 ns; t _{PHL354} is 5 to 90 ns; t _{PHL355} is 5 to 90 ns; t _{PHL356} is 5 to 90 ns; t _{PHL357} is 5 to 90 ns; t _{PHL358} is 5 to 90 ns; t _{PHL359} is 5 to 90 ns; t _{PHL360} is 5 to 90 ns; t _{PHL361} is 5 to 90 ns; t _{PHL362} is 5 to 90 ns; t _{PHL363} is 5 to 90 ns; t _{PHL364} is 5 to 90 ns; t _{PHL365} is 5 to 90 ns; t _{PHL366} is 5 to 90 ns; t _{PHL367} is 5 to 90 ns; t _{PHL368} is 5 to 90 ns; t _{PHL369} is 5 to 90 ns; t _{PHL370} is 5 to 90 ns; t _{PHL371} is 5 to 90 ns; t _{PHL372} is 5 to 90 ns; t _{PHL373} is 5 to 90 ns; t _{PHL374} is 5 to 90 ns; t _{PHL375} is 5 to 90 ns; t _{PHL376} is 5 to 90 ns; t _{PHL377} is 5 to 90 ns; t _{PHL378} is 5 to 90 ns; t _{PHL379} is 5 to 90 ns; t _{PHL380} is 5 to 90 ns; t _{PHL381} is 5 to 90 ns; t _{PHL382} is 5 to 90 ns; t _{PHL383} is 5 to 90 ns; t _{PHL384} is 5 to 90 ns; t _{PHL385} is 5 to 90 ns; t _{PHL386} is 5 to 90 ns; t _{PHL387} is 5 to 90 ns; t _{PHL388} is 5 to 90 ns; t _{PHL389} is 5 to 90 ns; t _{PHL390} is 5 to 90 ns; t _{PHL391} is 5 to 90 ns; t _{PHL392} is 5 to 90 ns; t _{PHL393} is 5 to 90 ns; t _{PHL394} is 5 to 90 ns; t _{PHL395} is 5 to 90 ns; t _{PHL396} is 5 to 90 ns; t _{PHL397} is 5 to 90 ns; t _{PHL398} is 5 to 90 ns; t _{PHL399} is 5 to 90 ns; t _{PHL400} is 5 to 90 ns; t _{PHL401} is 5 to 90 ns; t _{PHL402} is 5 to 90 ns; t _{PHL403} is 5 to 90 ns; t _{PHL404} is 5 to 90 ns; t _{PHL405} is 5 to 90 ns; t _{PHL406} is 5 to 90 ns; t _{PHL407} is 5 to 90 ns; t _{PHL408} is 5 to 90 ns; t _{PHL409} is 5 to 90 ns; t _{PHL410} is 5 to 90 ns; t _{PHL411} is 5 to 90 ns; t _{PHL412} is 5 to 90 ns; t _{PHL413} is 5 to 90 ns; t _{PHL414} is 5 to 90 ns; t _{PHL415} is 5 to 90 ns; t _{PHL416} is 5 to 90 ns; t _{PHL417} is 5 to 90 ns; t _{PHL418} is 5 to 90 ns; t _{PHL419} is 5 to 90 ns; t _{PHL420} is 5 to 90 ns; t _{PHL421} is 5 to 90 ns; t _{PHL422} is 5 to 90 ns; t _{PHL423} is 5 to 90 ns; t _{PHL424} is 5 to 90 ns; t _{PHL425} is 5 to 90 ns; t _{PHL426} is 5 to 90 ns; t _{PHL427} is 5 to 90 ns; t _{PHL428} is 5 to 90 ns; t _{PHL429} is 5 to 90 ns; t _{PHL430} is 5 to 90 ns; t _{PHL431} is 5 to 90 ns; t _{PHL432} is 5 to 90 ns; t _{PHL433} is 5 to 90 ns; t _{PHL434} is 5 to 90 ns; t _{PHL435} is 5 to 90 ns; t _{PHL436} is 5 to 90 ns; t _{PHL437} is 5 to 90 ns; t _{PHL438} is 5 to 90 ns; t _{PHL439} is 5 to 90 ns; t _{PHL440} is 5 to 90 ns; t _{PHL441} is 5 to 90 ns; t _{PHL442} is 5 to 90 ns; t _{PHL443} is 5 to 90 ns; t _{PHL444} is 5 to 90 ns; t _{PHL445} is 5 to 90 ns; t _{PHL446} is 5 to 90 ns; t _{PHL447} is 5 to 90 ns; t _{PHL448} is 5 to 90 ns; t _{PHL449} is 5 to 90 ns; t _{PHL450} is 5 to 90 ns; t _{PHL451} is 5 to 90 ns; t _{PHL452} is 5 to 90 ns; t _{PHL453} is 5 to 90 ns; t _{PHL454} is 5 to 90 ns; t _{PHL455} is 5 to 90 ns; t _{PHL456} is 5 to 90 ns; t _{PHL457} is 5 to 90 ns; t _{PHL458} is 5 to 90 ns; t _{PHL459} is 5 to 90 ns; t _{PHL460} is 5 to 90 ns; t _{PHL461} is 5 to 90 ns; t _{PHL462} is 5 to 90 ns; t _{PHL463} is 5 to 90 ns; t _{PHL464} is 5 to 90 ns; t _{PHL465} is 5 to 90 ns; t _{PHL466} is 5 to 90 ns; t _{PHL467} is 5 to 90 ns; t _{PHL468} is 5 to 90 ns; t _{PHL469} is 5 to 90 ns; t _{PHL470} is 5 to 90 ns; t _{PHL471} is 5 to 90 ns; t _{PHL472} is 5 to 90 ns; t _{PHL473} is 5 to 90 ns; t _{PHL474} is 5 to 90 ns; t _{PHL475} is 5 to 90 ns; t _{PHL476} is 5 to 90 ns; t _{PHL477} is 5 to 90 ns; t _{PHL478} is 5 to 90 ns; t _{PHL479} is 5 to 90 ns; t _{PHL480} is 5 to 90 ns; t _{PHL481} is 5 to 90 ns; t _{PHL482} is 5 to 90 ns; t _{PHL483} is 5 to 90 ns; t _{PHL484} is 5 to 90 ns; t _{PHL485} is 5 to 90 ns; t _{PHL486} is 5 to 90 ns; t _{PHL487} is 5 to 90 ns; t _{PHL488} is 5 to 90 ns; t _{PHL489} is 5 to 90 ns; t _{PHL490} is 5 to 90 ns; t _{PHL491} is 5 to 90 ns; t _{PHL492} is 5 to 90 ns; t _{PHL493} is 5 to 90 ns; t _{PHL494} is 5 to 90 ns; t _{PHL495} is 5 to 90 ns; t _{PHL496} is 5 to 90 ns; t _{PHL497} is 5 to 90 ns; t _{PHL498} is 5 to 90 ns; t _{PHL499} is 5 to 90 ns; t _{PHL500} is 5 to 90 ns; t _{PHL501} is 5 to 90 ns; t _{PHL502} is 5 to 90 ns; t _{PHL503} is 5 to 90 ns; t _{PHL504} is 5 to 90 ns; t _{PHL505} is 5 to 90 ns; t _{PHL506} is 5 to 90 ns; t _{PHL507} is 5 to 90 ns; t _{PHL508} is 5 to 90 ns; t _{PHL509} is 5 to 90 ns; t _{PHL510} is 5 to 90 ns; t _{PHL511} is 5 to 90 ns; t _{PHL512} is 5 to 90 ns; t _{PHL513} is 5 to 90 ns; t _{PHL514} is 5 to 90 ns; t _{PHL515} is 5 to 90 ns; t _{PHL516} is 5 to 90 ns; t _{PHL517} is 5 to 90 ns; t _{PHL518} is 5 to 90 ns; t _{PHL519} is 5 to 90 ns; t _{PHL520} is 5 to 90 ns; t _{PHL521} is 5 to 90 ns; t _{PHL522} is 5 to 90 ns; t _{PHL523} is 5 to 90 ns; t _{PHL524} is 5 to 90 ns; t _{PHL525} is 5 to 90 ns; t _{PHL526} is 5 to 90 ns; t _{PHL527} is 5 to 90 ns; t _{PHL528} is 5 to 90 ns; t _{PHL529} is 5 to 90 ns; t _{PHL530} is 5 to 90 ns; t _{PHL531} is 5 to 90 ns; t _{PHL532} is 5 to 90 ns; t _{PHL533} is 5 to 90 ns; t _{PHL534} is 5 to 90 ns; t _{PHL535} is 5 to 90 ns; t _{PHL536} is 5 to 90 ns; t _{PHL537} is 5 to 90 ns; t _{PHL538} is 5 to 90 ns; t _{PHL539} is 5 to 90 ns; t _{PHL540} is 5 to 90 ns; t _{PHL541} is 5 to 90 ns; t _{PHL542} is 5 to 90 ns; t _{PHL543} is 5 to 90 ns; t _{PHL544} is 5 to 90 ns; t _{PHL545} is 5 to 90 ns; t _{PHL546} is 5 to 90 ns; t _{PHL547} is																						

NOTES:

- A. $V_{IN} = 3.0\text{ V}$ minimum.
- B. $V_{IN} = 0.0\text{ V}$ or GND.
- C. Apply input pulse
- 
- D. Test numbers 39 through 58 shall be run in sequence.
- E. Output voltages shall be either:
- $H > 1.5\text{ V}; L < 1.5\text{ V}$
- F. Rext = 1.4 k Ω minimum to 70 k Ω maximum, connected to V_{CC} ; Cext $\leq 1,000\text{ }\mu\text{F}$, connected to Rext terminal.
- G. Rext = 2 k $\Omega \pm 10\%$, connected to V_{CC} ; Cext = 80 pF $\pm 10\%$, connected to Rext terminal.
- I. Rext = 10 k $\Omega \pm 10\%$, connect to V_{CC} ; Cext = 100 pF $\pm 10\%$, connected to Rext terminal.
- J. Rext = 10 k $\Omega \pm 10\%$, connect to V_{CC} ; Cext = 1.0 $\mu\text{F} \pm 10\%$, connected to Rext terminal.
- K. Rext = 2 k $\Omega \pm 10\%$, connect to V_{CC} .
- L. Note F may apply during subgroups 1, 2, and 3 testing if desired.
- M. During subgroups 9, 10, 11 testing, Rext and Cext may remain applied on the side of the device not under test if desired.
- N. Rext = 10 k $\Omega \pm 10\%$, connect to V_{CC} ; Cext $\geq 45\text{ }\mu\text{F}$ connected to Rext terminal.

TABLE III. Group A inspection for device type 03.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.7 V, or open).

Subgroup	Symbol	MIL-STD-883 method	Case A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured terminal	Limits	Unit
1 $T_C = 25^\circ\text{C}$	V_{OH}	3006 3006	Case 2	A1	A2	B1	B2	CLR	$\bar{Q}$	GND	Q	Rint	NC	Cext (see I)	NC		V _{CC}			
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	Q	2.5	V
	V_{OL}	3007 3007	Case 2																	
				3	4	5	6	7	8	9	10	11	12	13	14	15	16	$\bar{Q}$	2.5	V
	I _C																			
				5	6	7	8	9	10	11	12	13	14	15	16	17	18	A1	-1.5	"
				7	8	9	10	11	12	13	14	15	16	17	18	19	20	A2	"	"
				8	9	10	11	12	13	14	15	16	17	18	19	20	21	B1	"	"
				9	10	11	12	13	14	15	16	17	18	19	20	21	22	B2	"	"
				10	11	12	13	14	15	16	17	18	19	20	21	22	23	CLR	"	"
2 $T_C = 25^\circ\text{C}$	I _{L1} see note C	3009																		
				10	11	12	13	14	15	16	17	18	19	20	21	22	23	A1	-160	μA
				11	12	13	14	15	16	17	18	19	20	21	22	23	24	A2	"	"
				12	13	14	15	16	17	18	19	20	21	22	23	24	25	B1	"	"
				13	14	15	16	17	18	19	20	21	22	23	24	25	26	B2	"	"
				14	15	16	17	18	19	20	21	22	23	24	25	26	27	CLR	"	"
				15	16	17	18	19	20	21	22	23	24	25	26	27	28	A1	20	"
				16	17	18	19	20	21	22	23	24	25	26	27	28	29	A2	"	"
3 $T_C = 25^\circ\text{C}$	I _{H2}																			
				20	21	22	23	24	25	26	27	28	29	30	31	32	33	A1	100	"
				21	22	23	24	25	26	27	28	29	30	31	32	33	34	A2	"	"
				22	23	24	25	26	27	28	29	30	31	32	33	34	35	B1	"	"
				23	24	25	26	27	28	29	30	31	32	33	34	35	36	B2	"	"
				24	25	26	27	28	29	30	31	32	33	34	35	36	37	CLR	"	"
				25	26	27	28	29	30	31	32	33	34	35	36	37	38	Q	-15	mA
				26	27	28	29	30	31	32	33	34	35	36	37	38	39	Q	-100	"
7 $T_C = 25^\circ\text{C}$	CC1	3005																		
				27	28	29	30	31	32	33	34	35	36	37	38	39	40	Q	-30	"
				28	29	30	31	32	33	34	35	36	37	38	39	40	41	$\bar{Q}$	-15	"
				29	30	31	32	33	34	35	36	37	38	39	40	41	42	$\bar{Q}$	-100	"
				30	31	32	33	34	35	36	37	38	39	40	41	42	43	Q	-30	"
				31	32	33	34	35	36	37	38	39	40	41	42	43	44	V _{CC}	11	"
				32	33	34	35	36	37	38	39	40	41	42	43	44	45	V _{CC}	11	"
				33	34	35	36	37	38	39	40	41	42	43	44	45	46	V _{CC}	11	"
				34	35	36	37	38	39	40	41	42	43	44	45	46	47	V _{CC}	11	"

See footnotes at end of this table.

TABLE III. Group A inspection for device type 03 – Continued.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.7 V, or open).

Subgroup	Symbol	MIL-STD-883 method	Case A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured terminal	Limits	Unit
			Case 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20		Min	Max
7	Tc = 25°C		Test no.	A1	A2	B1	B2	CLR	$\bar{Q}$	GND	Q	Rint	NC	Cext (see I)	NC	Rext Cext (see I)	Vcc			
			36	B	A	A	A	A	L	GND	H			K		K	5.0 V	All outputs		See notes A, B
			37	"	"	"	B	B	H	"	L			"		"	"	"		
			38	"	"	"	B	A	H	"	L			"		"	"	"		
			39	"	"	"	A	A	L	"	H			"		"	"	"		
			40	"	"	"	"	B	H	"	L			"		"	"	"		
			41	A	"	"	"	A	H	"	L			"		"	"	"		
			42	B	"	"	"	A	L	"	H			"		"	"	"		
			43	A	"	"	"	B	H	"	L			"		"	"	"		
			44	"	"	"	"	A	H	"	L			"		"	"	"		
			45	"	B	"	"	A	L	"	H			"		"	"	"		
8			Repeat subgroup 7 at Tc = +125°C and Tc = -55°C.																	
1	t_{PHL1}	3003		IN	5.0 V	5.0 V	5.0 V	5.0 V	OUT	GND				K		K	5.0 V	A1 to $\bar{Q}$	5	50
	t_{PHL2}	(Fig. 3)		GND	GND	IN	5.0 V	5.0 V	OUT	"				K		K	"	B1 to $\bar{Q}$		61
	t_{PHL3}	"		GND	GND	GND	5.0 V	IN		"	OUT			M		M	"	CLR to Q		32
	t_{PHL4}	"		IN	5.0 V	5.0 V	5.0 V	5.0 V		"	OUT			K		K	"	A1 to Q		38
	t_{PLH2}	"		GND	GND	IN	5.0 V	5.0 V		"	OUT			K		K	"	B1 to Q		49
	t_{PLH3}	"		GND	GND	5.0 V	IN	IN	OUT	"				M		M	"	CLR to $\bar{Q}$		50
	t_{PLH4}	"		IN	5.0 V	5.0 V	5.0 V	5.0 V		"	OUT					G	"	Q		205
	t_{P4}	"		GND	GND	IN	5.0 V	5.0 V		"	OUT			F		N	"	Q	3.5	6.0
10	Same tests and terminal conditions as subgroup 9 except Tc = +125°C and limits are as follows: t_{PHL1} is 50 to 75 ns; t_{PHL2} is 5 to 92 ns; t_{PHL3} is 5 to 48 ns; t_{PH4} is 5 to 57 ns;																			
11	Same tests and terminal conditions as subgroup 10 except Tc = -55°C.																			

NOTES:

- A. $V_{IN} = 3.0$ V minimum.
 B. $V_{IN} = 0.0$ V or GND.
 C. For circuit D, IIL1 limits are 120 to 360 mA.
 D. Test numbers 29 through 45 shall be run in sequence.
 E. Output voltages shall be either:
 H > 1.5 V; L < 1.5 V
 F. Cext connected to Rext/Cext through a 1,000 pF $\pm 10\%$ capacitor.
 G. Rext/Cext connected to Vcc through a 5 k Ω resistor, and Cext connected to Rext/Cext through a $\leq 1,000$ μ F capacitor.
 I. Note K may apply during subgroups 1, 2, and 3 testing if desired.
 J. Apply input pulse
 

- K. Rext/Cext connected to Vcc through a 5 k Ω to 180 k Ω resistor, and Cext connected to Rext/Cext through a $\leq 1,000$ μ F capacitor.
 M. Cext connected to Rext/Cext through A ≥ 45 pF capacitor, Rext/Cext connected to Vcc through a 10 k Ω $\pm 10\%$ resistor.
 N. Rext/Cext connected to Vcc through a 10 k Ω $\pm 10\%$ resistor.

5. PACKAGING

5.1 Packaging requirements. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

(This section contains information of a general or explanatory nature which may be helpful, but is not mandatory.)

6.1 Intended use. Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.2 Acquisition requirements. Acquisition documents should specify the following:

- a. Title, number, and date of the specification.
- b. Complete part number (see 1.2).
- c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
- d. Requirements for certificate of compliance, if applicable.
- e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
- f. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
- g. Requirements for product assurance options.
- h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
- j. Requirements for "JAN" marking.

6.3 Superseding information. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.

6.4 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.

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6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND	Ground zero voltage potential
I _{IN}	Current flowing into an input terminal
V _{IC}	Input clamp voltage
V _{IN}	Voltage level at an input terminal

6.6 Logistic support. Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming should not affect the part number.

6.7 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information should not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device type	Generic-industry type
01	54LS123, 74LS123
02	54LS221, 74LS221
03	54LS122, 74LS122

6.8 Manufacturers' designation. Manufacturers' circuits, which form a part of this specification, are designated as shown in table IV herein.

TABLE IV. Manufacturers' designation.

Manufacturers					
Device type	A	B	C	D	E
	Texas Instruments Inc.	Signetics Corporation	National Semiconductor Corp	Motorola Inc	Raytheon Company
01	X		X	X	
02	X	X	X	X	
03	X		X	X	X

6.9 Changes from previous issue. Asterisks are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians:
Army - CR
Navy - EC
Air Force - 11
DLA - CC

Preparing activity:
DLA - CC

(Project 5962-1964)

Review activities:
Army - MI, SM
Navy - AS, CG, MC, SH, TD
Air Force - 03, 19, 99

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1. DOCUMENT NUMBER
MIL-M-38510/314C

2. DOCUMENT DATE (YYYYMMDD)
2003-07-14

3. DOCUMENT TITLE

MICROCIRCUITS, DIGITAL, LOW-POWER SCHOTTKY TTL, MONOSTABLE MULTIVIBRATORS, MONOLITHIC SILICON

4. NATURE OF CHANGE *(Identify paragraph number and include proposed rewrite, if possible. Attach extra sheets as needed.)*

5. REASON FOR RECOMMENDATION

6. SUBMITTER

a. NAME *(Last, First Middle Initial)*

b. ORGANIZATION

c. ADDRESS *(Include Zip Code)*

d. TELEPHONE *(Include Area Code)*
(1) Commercial
(2) DSN
(If applicable)

7. DATE SUBMITTED
(YYYYMMDD)

8. PREPARING ACTIVITY

a. NAME
Defense Supply Center, Columbus

b. TELEPHONE *(Include Area Code)*
(1) Commercial 614-692-0536 (2) DSN 850-0536

c. ADDRESS *(Include Zip Code)*
DSCC-VA
P. O. Box 3990
Columbus, Ohio 43216-5000

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